



## PRODUCT SPECIFICATION

*Part Number*

PG19248A-O Series

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	
APPROVED BY	
DATE	

## Table of Contents

[illegible]



**PAGE**

SPEC ONLY

3

[illegible]

## 1. FUNCTIONS & FEATURES

1.1. Format	: 192x48 dots
1.2. LCD mode	: STN /Positive Transflective Mode/Y-G
1.3. Viewing direction	: 6 o'clock
1.4. Driving scheme	: 1/48 Duty cycle, 1/8 Bias
1.5. Power supply voltage (V <sub>DD</sub> )	: 5.0V
1.6. LCD driving voltage	: 9.0V(Reference voltage)
1.7. Operation temp	: 0~+50°C
1.8. Storage temp	: -20~+70°C
1.9. Backlight color	: Yellow-Green
1.10 Control IC	: SBN0064G
1.11.RoHS standard	

## 2. MECHANICAL SPECIFICATIONS

2.1. Module size	: 122.4mm(L)*52.7mm(W)*13.1max mm (H)
2.2. Viewing area	: 99.0mm(L)*24.0mm(W)
2.3. Dot pitch	: 0.49mm(L)*0.46mm(W)
2.4. Dot size	: 0.44mm(L)*0.41mm(W)
2.5. Weight	: Approx.

## 3. BLOCK DIAGRAM

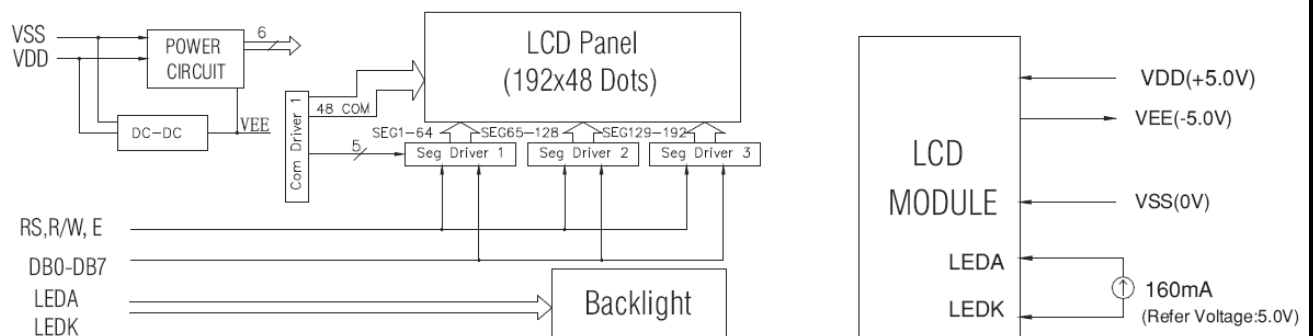


Figure 1. Block diagram

Figure 1. Dimensional outline

## **5. PIN DESCRIPTION**

No.	Symbol	Function
1	VSS	GND (0V)
2	VDD	Power supply for logic(+5.0V)
3	VEE	Output of supply negative voltage by the DC-DC converter on the module
4	RS	Register selection. (H: Data register L: Instruction register)
5	R/W	Read / write selection. (H: Read L: write)
6	E	Enable signal for chip
7~14	DB0~DB7	Data bus lines
15	LED A	Power supply for backlight(+5.0V)
16	LED K	Power supply for backlight(0V)
17	LED1	NO connection
18	LED2	
19	R/W	NO connection
20	E	

## **6. MAXIMUM ABSOLUTE LIMIT**

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V <sub>DD</sub>	voltage on the V <sub>DD</sub> pin(pad)	-0.3	+7.0	volt
V <sub>EE</sub>	voltage on the V <sub>EE</sub> pin(pad)	V <sub>DD</sub> - 16		
V <sub>LCD</sub> (note 2)	LCD bias voltage, V <sub>LCD</sub> =V0-V5		13	
V <sub>I</sub>	input voltage on any pin with respect to V <sub>SS</sub>	-0.3	V <sub>DD</sub> + 0.3	
P <sub>D</sub>	power dissipation		200	mW
T <sub>stg</sub>	storage temperature range	-55	+125	°C
T <sub>amb</sub>	operating ambient temperature range	-30	+ 85	°C
Tsol (note 3)	soldering temperature/time at pin		260 °C, 10 Second	

### **Notes**

- The following applies to the Absolute Maximum Rating:
  - Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device.
  - The SBN0064G includes circuitry specifically designed for the protection of its internal devices from the damaging effect of excessive static charge (ESD). However, it is suggested that conventional precautions be taken to avoid applying greater than the rated maxima.
  - Parameters are valid over operating temperature range unless otherwise specified.
  - All voltages are with respect to V<sub>SS</sub>, unless otherwise noted.
- The condition V<sub>DD</sub>(V0) ≥ V1 ≥ V2 ≥ V3 ≥ V4 ≥ V5 must always be met.
- QFP-type packages are sensitive to moisture of the environment, please check the drypack indicator on the tray package before soldering. Exposure to moisture longer than the rated drypack level may lead to cracking of the plastic package or broken bonding wiring inside the chip.

## 7. ELECTRICAL CHARACTERISTICS

### 7.1. DC characteristics

$V_{DD} = 5\text{ V} \pm 10\%$ ;  $V_{SS} = 0\text{ V}$ ; all voltages with respect to  $V_{SS}$ , unless otherwise specified;  $T_{amb} = -20\text{ to }+75\text{ }^{\circ}\text{C}$ .

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{DD}$	Supply voltage for logic		2.7	5.0	5.5	V
$V_{NEG}$	$V_{NEG} = V_{DD} - V_{EE}$				16	V
$V_{LCD}$	LCD bias voltage $V_{LCD} = V_0(V_{DD}) - V_5$	Note 1.			13	V
$V_{IL}$	LOW level input voltage	For all inputs	0		0.8	V
$V_{IH}$	HIGH level input voltage	For all inputs	$V_{DD} - 2.2$		$V_{DD}$	V
$V_{OL}$	LOW level output voltage of DB0~7 at $I_{OL} = 1.6\text{ mA}$ .		0.0		0.3	V
$V_{OH}$	HIGH level output voltage of DB0~7 at $I_{OH} = -200\text{ }\mu\text{A}$ .		$V_{DD} - 0.3$		$V_{DD}$	V
$I_{LKG}$	Leakage current of input pins	for all inputs			0.2	$\mu\text{A}$
$I_{STBY}$	Stand-by current at $V_{DD} = 5\text{ volts}$	Note 2			3.0	$\mu\text{A}$
$I_{DD(1)}$	Operating current for display-only operation	Note 3			100	$\mu\text{A}$
$I_{DD(2)}$	Operating current for display and microcontroller access at $t_{CYC} = 1\text{ MHz}$	Note 4			500	$\mu\text{A}$
$C_{in}$	Input capacitance of all input pins			5.0	8.0	pF
$R_{ON}$	LCD driver ON resistance	Note 5		5.0	7.5	$\text{K}\Omega$

**Notes:**

- LCD bias voltage  $V_{LCD}$  is  $V_0 - V_5$ .  $V_0$  should always be connected to  $V_{DD}$ .
- Conditions for the measurement:  $\text{CLK1} = \text{CLK2} = V_{DD}$ , measured at the  $V_{DD}$  pin.
- This value is measured when the microcontroller does not perform any READ/WRITE operation to the chip and the chip is only performing display operation, with the following condition: 1/64 duty,  $F_{\text{CLK1,CLK2}} = 250\text{ KHz}$ , frame frequency = 70Hz, and no loading for SEG0~63.
- This values is measured when the microcontroller continuously performs READ/WRITE operation to the chip and the chip is also performing display operation with the following condition: 1/64 duty,  $F_{\text{CLK1,CLK2}} = 250\text{ KHz}$ , frame frequency = 70Hz, and no loading for SEG0~63.
- This measurement is for the transmission high-voltage PMOS or NMOS of SEG0~SEG63. Please refer to Section 16 for these driver circuit. The measurement is for the case when the voltage differential between the source and the drain of the high voltage PMOS or NMOS is 0.1 volts.



## 7.2. AC characteristics

### 7.2.1 writing timing

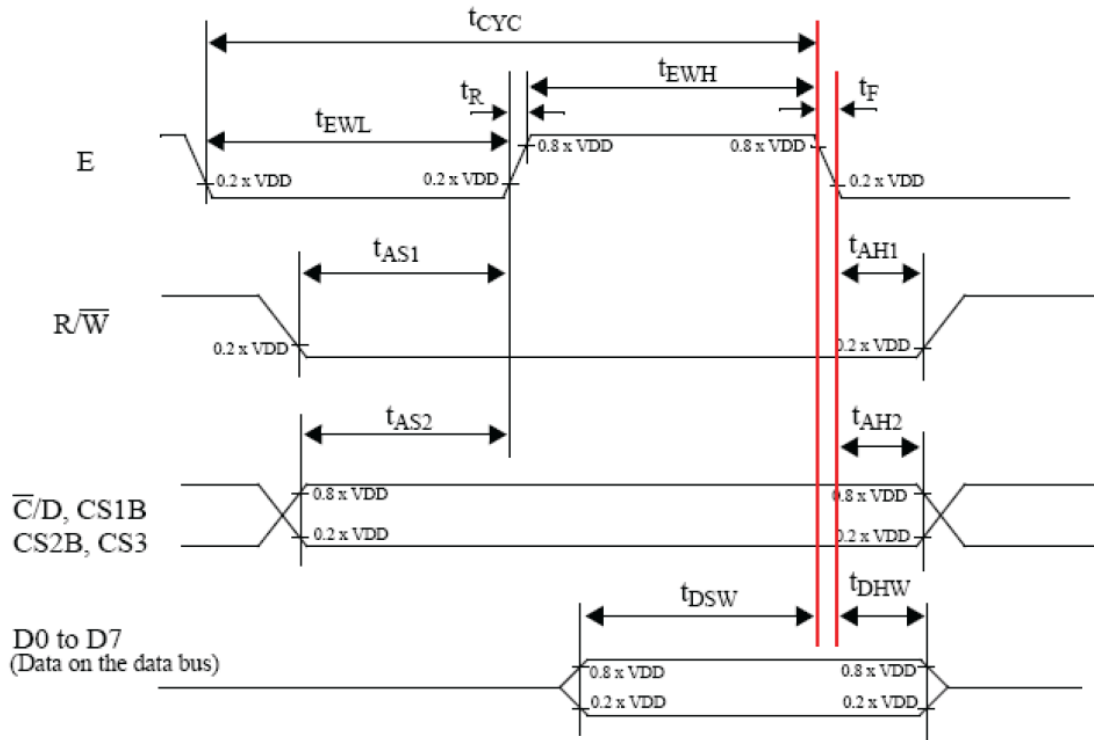


Fig.16 AC timing for writing to the SBN0064G

### 7.2.2 reading timing

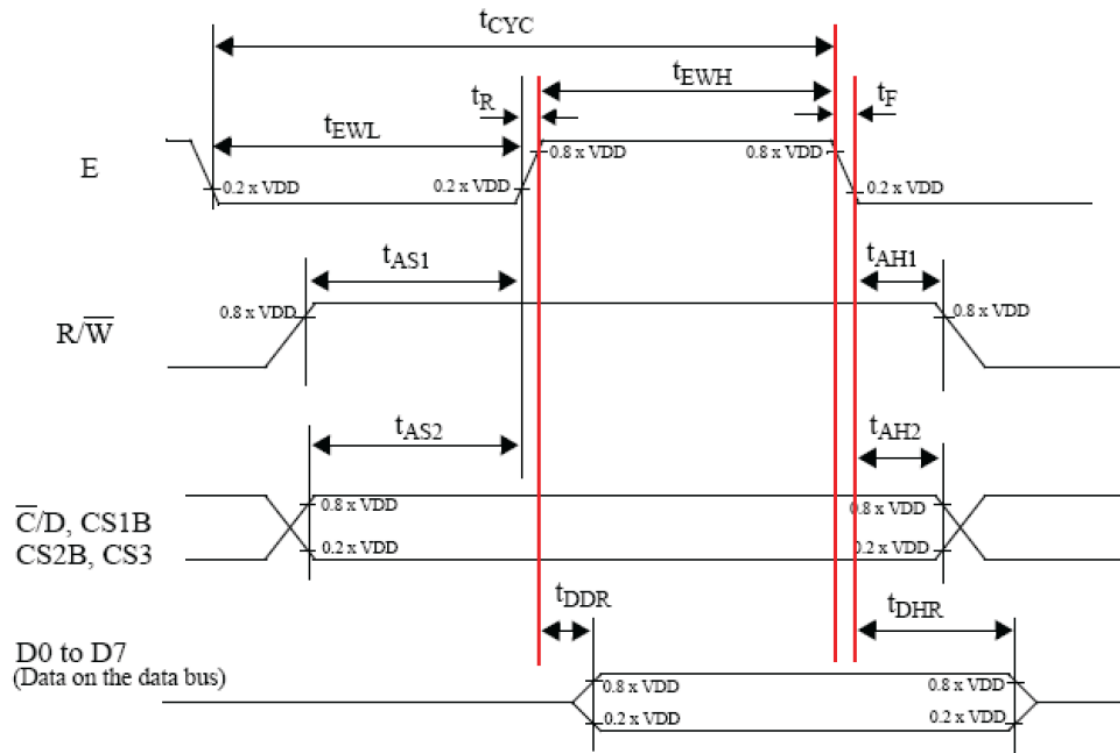


Fig.17 AC timing for reading from the SBN0064G

## 8. BACKLIGHT CHARACTERISTICS

Ta = 25°C

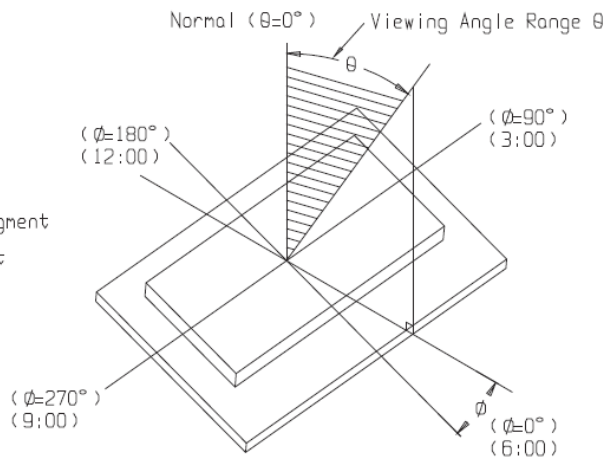
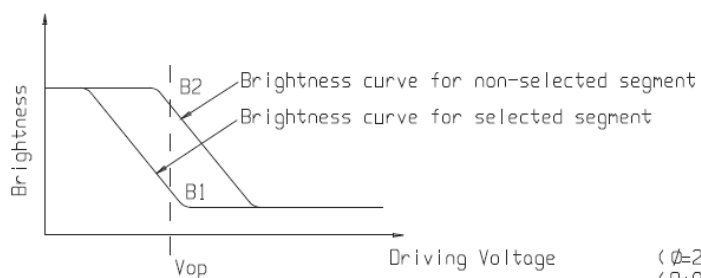
Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	VF	IF=160mA	---	4.2	---	V
Reverse Current	IR	VR=4.2 V	---	160	---	mA
Luminous Intensity (With LCD dots off)	IV		---	--	---	Cd/m <sup>2</sup>
Wave length(Without LCD)	λp	IF=160mA	570	---	575	nm
Color	Yellow-Green					

## 9. ELECTRO-OPTICAL CHARACTERISTICS

( VDD=5.0V, Ta = 25°C )

Item	Symbol	Condition	Min	Typ	Max	Unit
Operating Voltage	Vop	Ta = 25°C	8.7	9.0	9.3	V
Response time	Tr	Ta = 25°C	---	185	---	ms
	Tf		---	200	---	ms
Contrast	Cr	Ta = 25°C	---	4	---	---
Viewing angle range	θ	Cr≥2	-40	---	+40	deg
	Φ		-40	---	+40	deg

$$Cr = \frac{\text{Brightness of non-selected segment}(B2)}{\text{Brightness of selected segment}(B1)}$$

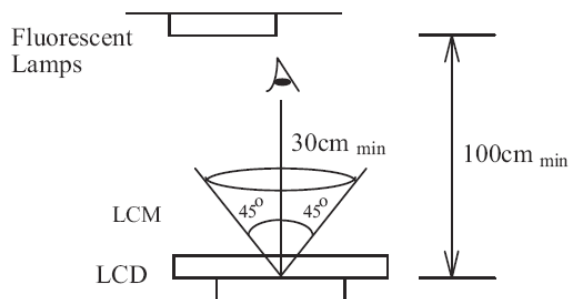


## **10.QUALITY SPECIFICATIONS**

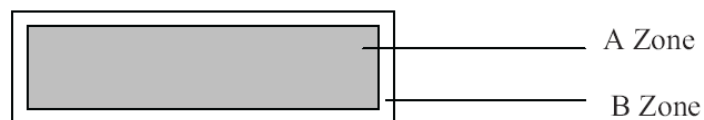
### **10.1 Standard of the product appearance test**

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).

B Zone: Non-active display area (outside viewing area).

### **10.2 Specification of quality assurance**

AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling

**P-TEC****MODEL NO.**

PG19248A-O series

SPEC ONLY

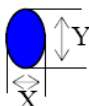
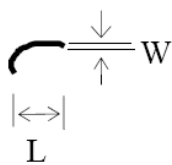
**PAGE**

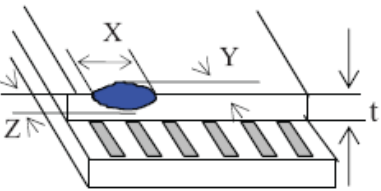
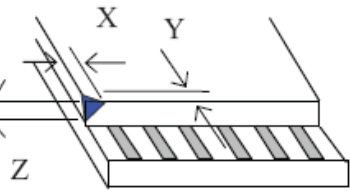
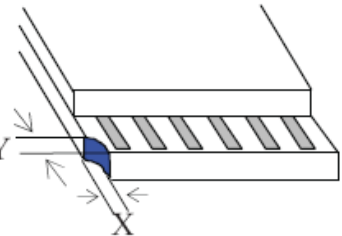
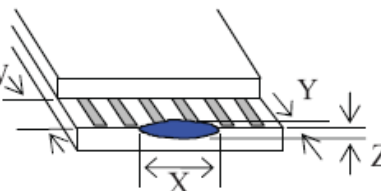
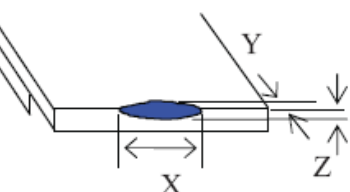
13

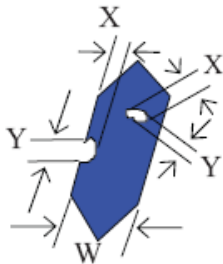
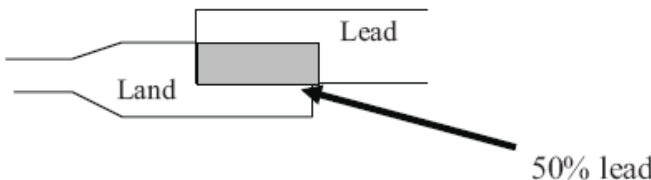
**Defect classification (Note: \* is not including)**

Classify	Item		Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display state	Background color deviation	2	1.0
		Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

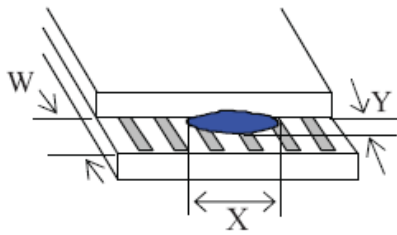
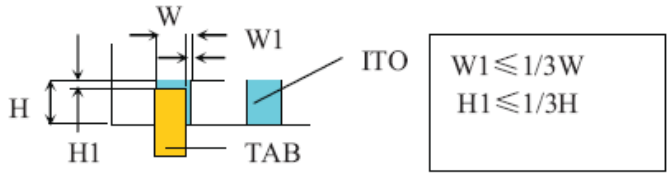
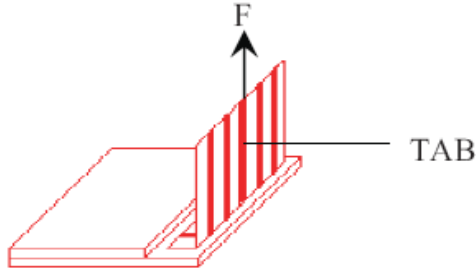
**Note on defect classification**

No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (including Polarizer)	<div></div> <table><thead><tr><th>Point Size</th><th>Acceptable Qty.</th></tr></thead><tbody><tr><td><math>\phi \leq 0.10</math></td><td>Disregard</td></tr><tr><td><math>0.10 &lt; \phi \leq 0.20</math></td><td>3</td></tr><tr><td><math>0.20 &lt; \phi \leq 0.25</math></td><td>2</td></tr><tr><td><math>0.25 &lt; \phi \leq 0.30</math></td><td>1</td></tr><tr><td><math>\phi &gt; 0.30</math></td><td>0</td></tr></tbody></table> <div><math>\phi = (X+Y)/2</math></div>	Point Size	Acceptable Qty.	$\phi \leq 0.10$	Disregard	$0.10 < \phi \leq 0.20$	3	$0.20 < \phi \leq 0.25$	2	$0.25 < \phi \leq 0.30$	1	$\phi > 0.30$	0								
	Point Size	Acceptable Qty.																				
$\phi \leq 0.10$	Disregard																					
$0.10 < \phi \leq 0.20$	3																					
$0.20 < \phi \leq 0.25$	2																					
$0.25 < \phi \leq 0.30$	1																					
$\phi > 0.30$	0																					
	Unit:mm																					
4	Line defect, Scratch	<div></div> <table><thead><tr><th colspan="2">Line</th><th>Acceptable Qty.</th></tr><tr><th>L</th><th>W</th><th></th></tr></thead><tbody><tr><td>---</td><td><math>0.015 \geq W</math></td><td>Disregard</td></tr><tr><td><math>3.0 \geq L</math></td><td><math>0.03 \geq W</math></td><td rowspan="2">2</td></tr><tr><td><math>2.0 \geq L</math></td><td><math>0.05 \geq W</math></td></tr><tr><td><math>1.0 \geq L</math></td><td><math>0.1 &gt; W</math></td><td>1</td></tr><tr><td>---</td><td><math>0.05 &lt; W</math></td><td>Applied as point defect</td></tr></tbody></table>	Line		Acceptable Qty.	L	W		---	$0.015 \geq W$	Disregard	$3.0 \geq L$	$0.03 \geq W$	2	$2.0 \geq L$	$0.05 \geq W$	$1.0 \geq L$	$0.1 > W$	1	---	$0.05 < W$	Applied as point defect
	Line		Acceptable Qty.																			
L	W																					
---	$0.015 \geq W$	Disregard																				
$3.0 \geq L$	$0.03 \geq W$	2																				
$2.0 \geq L$	$0.05 \geq W$																					
$1.0 \geq L$	$0.1 > W$	1																				
---	$0.05 < W$	Applied as point defect																				
	Unit: mm																					
5	Rainbow	Not more than two color changes across the viewing area.																				

No	Item	Criterion																																	
6	Chip  Remark: X: Length direction Y: Short direction Z: Thickness direction t: Glass thickness W: Terminal Width	<div>  <table border="1"> <caption>Acceptable criterion</caption> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td><math>\leq 2</math></td><td>0.5mm</td><td><math>\leq t/2</math></td></tr> </tbody> </table> </div> <div>  <table border="1"> <caption>Acceptable criterion</caption> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td><math>\leq 2</math></td><td>0.5mm</td><td><math>\leq t</math></td></tr> </tbody> </table> </div> <div>  <table border="1"> <caption>Acceptable criterion</caption> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td><math>\leq 3</math></td><td><math>\leq 2</math></td><td><math>\leq t</math></td></tr> <tr> <td colspan="2">shall not reach to ITO</td><td></td></tr> </tbody> </table> </div> <div>  <table border="1"> <caption>Acceptable criterion</caption> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td>Disregard</td><td><math>\leq 0.2</math></td><td><math>\leq t</math></td></tr> </tbody> </table> </div> <div>  <table border="1"> <caption>Acceptable criterion</caption> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td><math>\leq 5</math></td><td><math>\leq 2</math></td><td><math>\leq t/3</math></td></tr> </tbody> </table> </div>	X	Y	Z	$\leq 2$	0.5mm	$\leq t/2$	X	Y	Z	$\leq 2$	0.5mm	$\leq t$	X	Y	Z	$\leq 3$	$\leq 2$	$\leq t$	shall not reach to ITO			X	Y	Z	Disregard	$\leq 0.2$	$\leq t$	X	Y	Z	$\leq 5$	$\leq 2$	$\leq t/3$
X	Y	Z																																	
$\leq 2$	0.5mm	$\leq t/2$																																	
X	Y	Z																																	
$\leq 2$	0.5mm	$\leq t$																																	
X	Y	Z																																	
$\leq 3$	$\leq 2$	$\leq t$																																	
shall not reach to ITO																																			
X	Y	Z																																	
Disregard	$\leq 0.2$	$\leq t$																																	
X	Y	Z																																	
$\leq 5$	$\leq 2$	$\leq t/3$																																	

No.	Item	Criterion								
7	Segment pattern W = Segment width $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10\text{mm}$ is acceptable.  <table><tr><th>Point Size</th><th>Acceptable Qty</th></tr><tr><td><math>\phi \leq 1/4W</math></td><td>Disregard</td></tr><tr><td><math>1/4W &lt; \phi \leq 1/2W</math></td><td>1</td></tr><tr><td><math>\phi &gt; 1/2W</math></td><td>0</td></tr></table> <p>Unit: mm</p>	Point Size	Acceptable Qty	$\phi \leq 1/4W$	Disregard	$1/4W < \phi \leq 1/2W$	1	$\phi > 1/2W$	0
Point Size	Acceptable Qty									
$\phi \leq 1/4W$	Disregard									
$1/4W < \phi \leq 1/2W$	1									
$\phi > 1/2W$	0									
8	Back-light	(1) The color of backlight should correspond its specification. (2) Not allow flickering								
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. 								
10	Wire	(1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.								
11*	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.								



No	Item	Criterion
12	Protruded W: Terminal Width	 <p>Acceptable criteria: <math>Y \leq 0.4</math></p>
13	TAB	<p>1. Position</p>  <p>2 TAB bonding strength test</p>  <p><math>P (=F/TAB \text{ bonding width}) \geq 650\text{gf/cm}</math> ,(speed rate: 1mm/min) 5pcs per SOA (shipment)</p>
14	Total no. of acceptable Defect	<p>A. Zone</p> <p>Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm</p> <p>B. Zone</p> <p>It is acceptable when it is no trouble for quality and assembly in customer's end product.</p>

### 10.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	70°C	48	No abnormalities in functions and appearance
High temp. Operating	50°C	48	
Low temp. Storage	-20°C	48	
Low temp. Operating	0°C	48	
Humidity	40°C/ 90%RH	48	
Temp. Cycle	0°C ← 25°C → 50°C (30 min ← 5 min → 30min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance ,etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $20 \pm 8^{\circ}\text{C}$ ), normal humidity (below  $45 \pm 20\%$  RH), and in the area not exposed to direct sun light. The life time is not content the life time of the LED (for the life time of LED which decay only 50%,in the industry the experience value is 50000 hours, but there are not any experimentation data to support this).

### 10.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

#### General Precautions:

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol or trichlorotrifluoroethane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not make any modification on the PCB without consulting P-tec Corp.

5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

#### **Static Electricity Precautions:**


1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working benches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

#### **Soldering Precautions:**

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature:  $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$
4. Soldering time: 3 to 4 second.
5. Use eutectic solder with resin flux filling.
6. If flux is used, the LCD surface should be protected to avoid spattering flux.
7. Flux residue should be removed.

#### **Operation Precautions:**

1. The viewing angle can be adjusted by varying the LCD driving voltage  $V_o$ .
2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
4. Response time increases with decrease in temperature.
5. Display color may be affected at temperatures above its operational range.
6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
7. For long-term storage over  $40^{\circ}\text{C}$  is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

 <b>P-TEC</b>	MODEL NO.		PAGE
	PG19248A-O series	SPEC ONLY	20

### Limited Warranty

P-tec LCDs and modules are not consumer products, but may be incorporated by P-tec's customers into consumer products or components thereof, P-tec does not warrant that its LCDs and components are fit for any such particular purpose.

1. The liability of P-tec is limited to repair or replacement on the terms set forth below. P-tec will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between P-tec and the customer, P-tec will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with P-tec general LCD inspection standard .  
(Copies available on request)
2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.