

## **PRODUCT SPECIFICATION**

# Part Number PL00182-WCRR2121

#### Details

- Surface Mount LED
- 3.2mm x 2.7mm x 1.1mm
- Emitting color: Super Red
- AlGaAs chip material
- 3,000 Piece Reels

**Mechanical Dimensions** 

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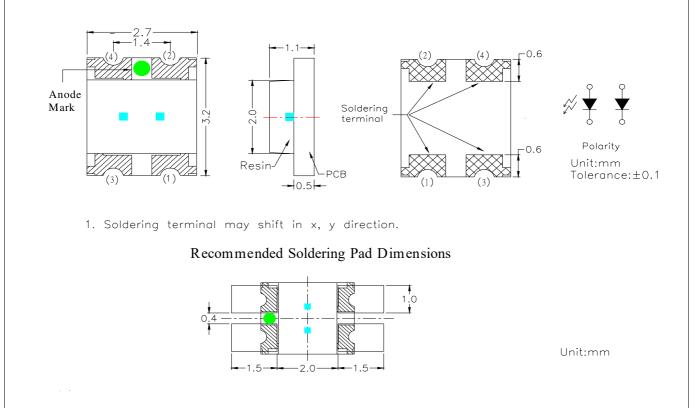
**Features** 

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• Compatible with automatic placement equipment

**RoHS** Compliant

• Compatible with reflow solder process





- 1. Dimensions in millimeters unless otherwise noted
- 2. Specifications subject to change without notice





### **Device Selection Guide**

Model Number		Chip	Lens Type
Model Number	Material	Emitting Color	
DL 00192 WCDD2121	AlGaAs	Super Red	Water Clear
PL00182-WCRR2121	AlGaAs	Super Red	

## Absolute Maximum Ratings at $Ta=25\,\mathcal{C}$

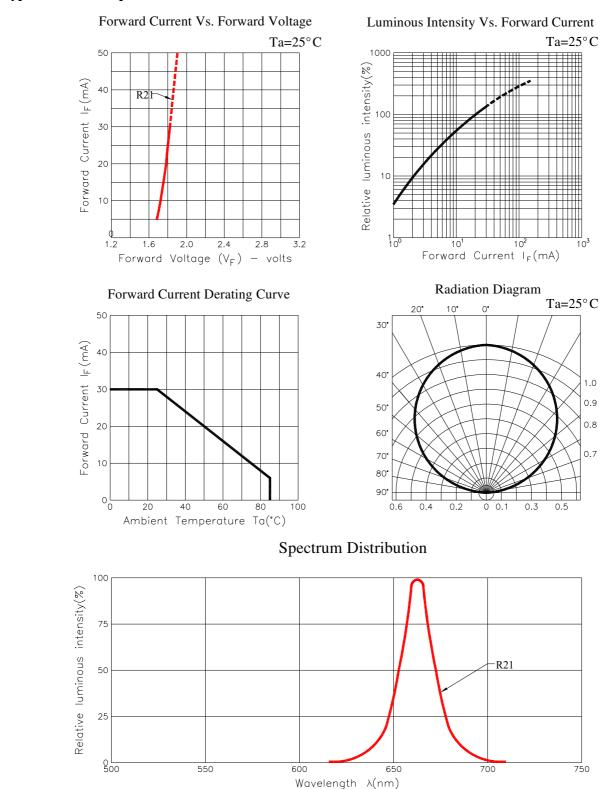
Parameter	Symbol	Maximum	Unit
Peak Forward Current (1/10 Duty Cycle 0.1ms Pulse Width)	IFP	100	mA
Derating Liner from 25°C		0.4	mA/°C
Reverse Voltage	VR	5	V
Operating Temperature	Topr	-40~+85	°C
Storage Temperature	Tstg	-40~+85	°C

### Electrical and Optical Characteristics at Ta=25 °C

	Chip		Lens	Abso	lute Max Ratings		El	ectro-op (At 20	otical D 0mA)	ata	Viewing
Emitted	λΡ	λD	Appearance	$\Delta\lambda$	PD	IFmax	VF	(V)	IV r	ncd)	Angle $2\theta 1/2$ (deg)
Color	(nm)	(nm)		(nm)	(mW)	(mA)	Тур.	Max.	Min.	Typ.	20172 (deg)
Super Red (Die 1)	660	643	Watar Class	20	69	30	1.9	2.3	7.0	18	1208
Super Red (Die 1)	660	643	Water Clear	20	69	30	1.9	2.3	7.0	18	120°

Notes: Tolerance Luminous intensity  $\pm 15\%$  and Wavelength ( $\lambda D$ )  $\pm 2nm$ 





## Typical Electro-Optical Characteristic Curves



## Luminous Intensity Bins

Bin Code	Test Condit	ion: @20mA
R21	Min. IV (mcd)	Max. Iv (mcd)
Е	7.2	11.5
F	11.5	18
G	18	28.5

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## Dominant Wavelength Bins

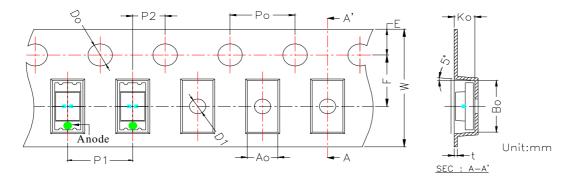
Bin Code	Test Condition: @20mA				
R21	λDmin (nm)	λDmax (nm)			
1	632	660			

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R21	λDmin (nm)	λDmax (nm)				
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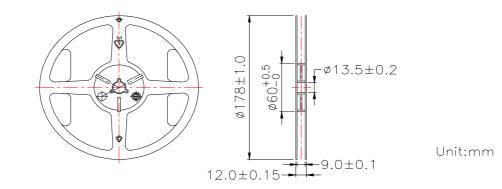


## Tape Specifications

	Packing Size												
Item	W	P1	E	F	Do	D1	Po	10Po	P2	Ao	Во	Ко	t
Spec.	8.00	4.00	1.75	3.50	1.50	1.00	4.00	40.00	2.00	2.75	3.35	1.35	0.23
Tolerance	±0.20	±0.10	±0.10	±0.05	+0.10 -0.00	±0.05	±0.05	±0.20	±0.05	±0.10	±0.10	±0.10	±0.05

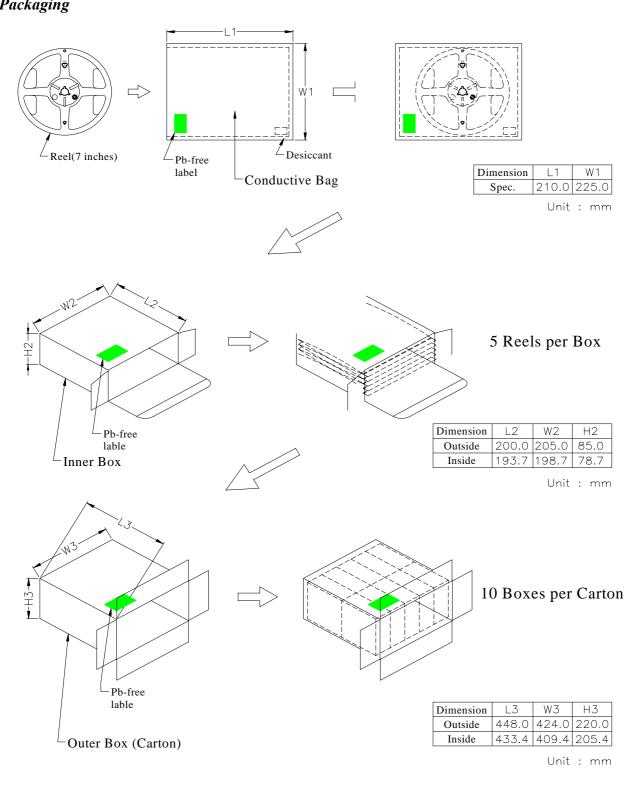


Packing and Shipping Specifications











#### **Precautions for Use**

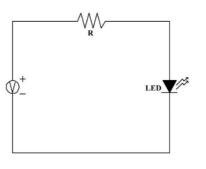
- The Chip-LED Taping is much smaller than lead frame type components, thus enable smaller board size, higher packing density, reduced storage space and finally smaller equipment to be obtained.
- Besides, lightweight makes them ideal for miniature application, etc.

No.	Item	Test Conditions	Test hr/cycle/time	Sample Q'ty	Ac / Re
1	Solder Heat	TEMP $: 260^{\circ}C \pm 5^{\circ}C : 10 \pm 1 \text{ sec}$	2 times	30 pcs	0/1
2	Solderbility Test 💥	TEMP : $235^{\circ}C \pm 5^{\circ}C$ ; $3 \pm 1$ sec	1 time	5 pcs	0/1
3	Temperature Cycle	H : +85°C 30min. $\int$ 5min. L : -40°C 30min.	100 cycles	20 pcs	0 / 1
4	Thermal Shock	$H : +85^{\circ}C  5min.$ $\int$ $L : -40^{\circ}C  5min.$	50 cycles	20 pcs	0 / 1
5	High Temperature Storage	TEMP : 85°C	1000 hrs	20 pcs	0/1
6	Low Temperature Storage	ТЕ <b>М</b> Р : -40°С	1000 hrs	20 pcs	0/1
7	DC Operating Life	$I_F = I_{Fmax}$	1000 hrs	20 pcs	0/1
8	High Temperature High Humidity	85℃ / 90~95%R.H.	1000 hrs	20 pcs	0 / 1
9	Shocking test	$\frac{100 \sim 2000 \text{Hz}  \text{; } 98.1 \text{m/s}^2}{\text{X,Y,Z direction}}$	2 hrs	20 pcs	0 / 1
10	Dropping test	Put on pallet ; height : 75cm	3 times	20 pcs	0/1
	•	Judgment Criteria		·	
	Forward Voltage V <sub>F</sub>		*		
	Reverse Current I <sub>R</sub>		-		
	Luminous Intensity I	v	$I_V \text{Decay} < 40\%$	6	
X. Co	Iderbility test criteria : covera	as is not loss than 05%			

X Solderbility test criteria : coverage is not less than 95%

Note : Measurement shall be taken after the tested samples have been returned to normal ambient conditions (generally after two hours)

#### Test Circuit





• Overdrive current proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause current change with great deal. (Burn out will happen)

• Storage

1. The operation of temperature and R.H. are :  $5^{\circ}C \sim 30^{\circ}C$ ,  $60^{\circ}R$ .H. Max.

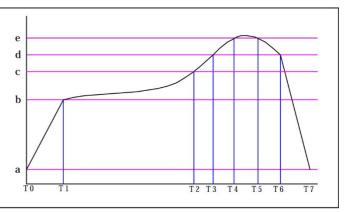
2. Once the package is opened, the products should be used within a week. Otherwise, they should be kept in a damp-proof box with desiccant. Considering the tape life, we suggest our customers use our

products within 1.5 years (from production date).

3. It is recommended to bake before soldering when the package is unsealed more than 72 hrs. The condition is:  $60^{\circ}C \pm 5^{\circ}C$  for 15 hrs.

#### **Reflow Temperature/Time**

TEM	<b>Р (°</b> С)	TIMI	E (sec)
а	25	T0~T1	5°C/sec max
b	150	T1~T2	90~130
с	200	T2~T3	5°C/sec max
d	230	T3~T6	60~90
e	260	T4~T5	10±1
		T6~T7	-6°C/sec max
MSL	level	Lev	vel 1



#### Hand Soldering Iron

Temperature at tip of iron: 400°C Max (35W Max) Soldering time: 3 +/-1 sec.



	<b>Approved By</b>	Checked By	<b>Prepared By</b>
PL00182-WCRR2121 Customer Approval Signatures			

Record of Revisions						
Rev.	Comments	Page	Date			
0	Released Spec		12/16/14			
1	Updated Spec		01/28/21			