

# **PRODUCT SPECIFICATION**

#### Part Number

# PG12864B-O Series

| CUSTOMER             |  |
|----------------------|--|
| CUSTOMER PART NUMBER |  |
| DESCRIPTION          |  |
| APPROVED BY          |  |
| DATE                 |  |



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# **Record of Revisions**

| Rev. | Comments                                    | Page | Date   |
|------|---|------|--------|
| 1    | Preliminary Specification was first issued. | All  | 7/4'14 |
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### 1. FUNCTIONS & FEATURES

1.1. Format : 128x64Dots

1.2. LCD mode : FSTN / Positive Transflective Mode

1.3. Viewing direction : 6 o'clock

1.4. Driving scheme : 1/64 Duty cycle, 1/9 Bias

1.5. Power supply voltage  $(V_{DD})$ : 5.0V1.6. LCD driving voltage: 9.0V1.7. Operation temp: -10~60°C1.8. Storage temp: -20~70°C1.9. Backlight color: White(EL)

1.10 RoHS

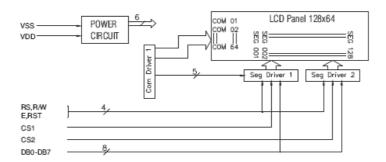
## 2. MECHANICAL SPECIFICATIONS

2.1. Module size : 54mm(L)\*50mm(W)\*7.5max mm(H)

2.2. Viewing area : 44.5mm(L)\*30mm(W)
2.3. Dot pitch : 0.32mm(L)\*0.39mm(W)
2.4. Dot size : 0.28mm(L)\*0.35mm(W)

2.5. Weight : Approx.

## 3. BLOCK DIAGRAM



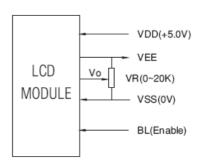


Figure 2. Block Diagram



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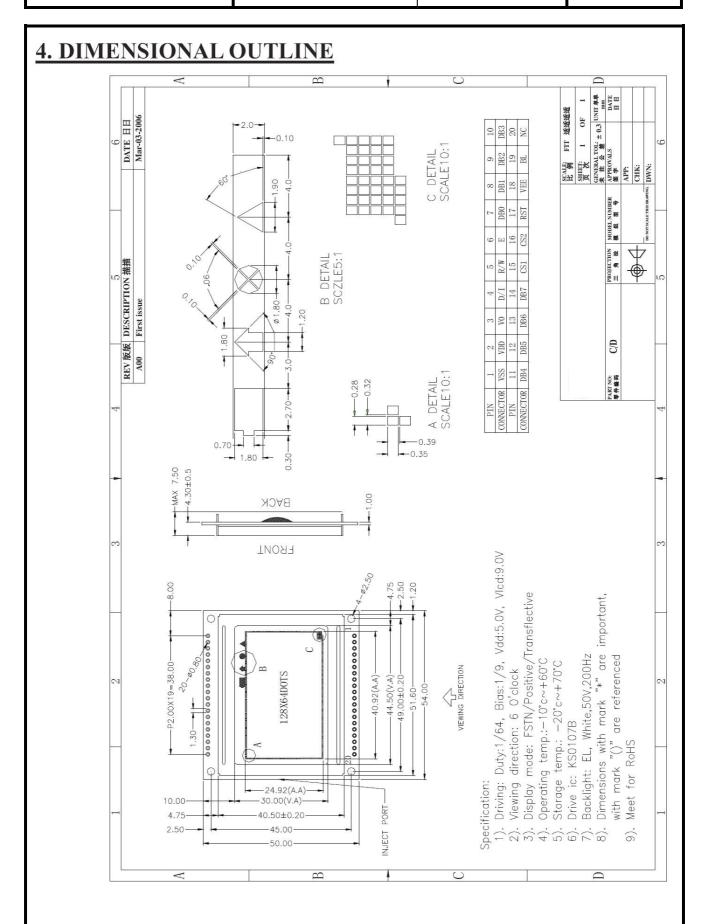


Figure 1. Dimensional Outline



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# **5. PIN DESCRIPTION**

| No.  | Symbol | Function   |
|------|--------|--|
| 1    | VSS    | GND  |
| 2    | VDD    | Power supply   |
| 3    | VO     | Supply voltage for LCD   |
| 4    | D/I    | Register selection. (H: Data register L: Instruction register) |
| 5    | R/W    | Read /write selection. (H: Read L: write)                      |
| 6    | Е      | Enable signal for chip   |
| 7-14 | D0~D7  | Data bus line  |
| 15   | CS1    | Chip select signal for left half of the screen(High select)    |
| 16   | CS2    | Chip select signal for right half of the screen(High select)   |
| 17   | RST    | Reset signal( L select)  |
| 18   | VEE    | Negative voltage output  |
| 19   | EL     | EL backlight enable signal(EL=H select)                        |
| 20   | NC     | No connected.  |

## **6. MAXIMUM ABSOLUTE LIMI**T

## (For IC)

| Characteristic        | Symbol           | Value                                      | Unit | Note |
|-----------------------|------------------|--|------|------|
| Operating Voltage     | V <sub>DO</sub>  | -0.3~+7.0                                  | V    | *1   |
| Supply Voltage        | V <sub>EE</sub>  | V <sub>DD</sub> -19.0~V <sub>DD</sub> +0.3 | V    | *4   |
| Driver Supply Voltage | Va               | -0.3~V <sub>DD</sub> +0.3                  | V    | *1,3 |
|                       | V <sub>LCD</sub> | V <sub>EE</sub> -0.3~V <sub>DD</sub> +0.3  | V    | *2   |
| Operating Temperature | T <sub>OPR</sub> | -30~+85                                    | ۰C   |      |
| Storage Temperature   | T <sub>STG</sub> | -55~+125                                   | °C   |      |

<sup>\*1.</sup> Based on Vss=0V.

<sup>\*2.</sup> Applies the same supply voltage to  $V_{\tt EE1}$  and  $V_{\tt EE2}$ .  $V_{\tt LCD} = V_{\tt DD} - V_{\tt EE}$ . \*3. Applies to M, FRM, CL, RSTB, ADC, CLK1, CLK2, CS1B, CS2B, CS3, E, R/W, RS and DB0~DB7.

<sup>\*4.</sup> Applies to V0L(R), V2L(R), V3L(R) and V5L(R).

Voltage level:  $V_{DD} \ge V0L = V0R \ge V2L = V2R \ge V3L = V3R \ge V5L = V5R \ge V_{EE}$ .



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### 7. ELECTRICAL CHARACTERISTICS

DC Characteristics ( $V_{DD}$ =+5 $V \pm 10\%$ ,  $V_{SS}$ =0V,  $V_{DD}$ - $V_{EE}$ =8~17V,  $T_a$ =-30~+85°C)

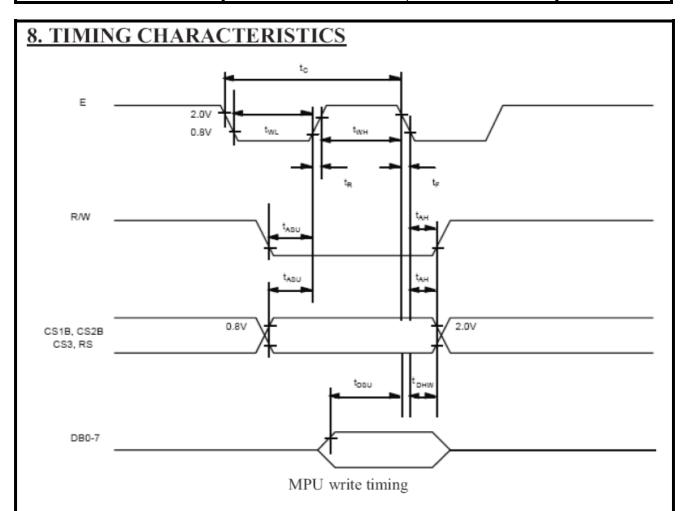
| Characteristic                    | Symbol           | Condition  | Min                | Тур | Max             | Unit | Note |
|-----------------------------------|------------------|--|--------------------|-----|-----------------|------|------|
| Input High Voltage                | V <sub>IH1</sub> | -  | 0.7V <sub>DD</sub> | -   | V <sub>DD</sub> | V    | *1   |
|                                   | V <sub>IH2</sub> | -  | 2.0                | -   | $V_{DD}$        | V    | *2   |
| Input Low Voltage                 | V <sub>IL1</sub> | -  | 0                  | -   | $0.3V_{DD}$     | V    | *1   |
|                                   | $V_{IL2}$        | -  | 0                  | -   | 8.0             | V    | *2   |
| Output High Voltage               | Vaн              | I <sub>OH</sub> =-200μA                                  | 2.4                | -   | -               | V    | *3   |
| Output Low Voltage                | VoL              | I <sub>oL</sub> =1.6mA                                   | -                  | -   | 0.4             | V    | *3   |
| Input Leakage Current             | I <sub>LKG</sub> | V <sub>IN</sub> =V <sub>SS</sub> ~V <sub>DD</sub>        | -1.0               | -   | 1.0             | μΑ   | *4   |
| Three-state(OFF) Input<br>Current | I <sub>TSL</sub> | V <sub>IN</sub> =V <sub>SS</sub> ~V <sub>DD</sub>        | -5.0               | -   | 5.0             | μА   | *5   |
| Driver Input Leakage Current      | I <sub>DIL</sub> | V <sub>IN</sub> =V <sub>EE</sub> ~V <sub>DD</sub>        | -2.0               | -   | 2.0             | μА   | *6   |
| Operating Current                 | I <sub>DD1</sub> | During Display   | -                  | -   | 100             | μА   | *7   |
|                                   | l <sub>DD2</sub> | During Access<br>Access Cycle=1MHz                       | -                  | -   | 500             | μА   | *7   |
| On Resistance                     | Ron              | $V_{DD}$ - $V_{EE}$ =15 $V$<br>$I_{LOAD}$ = $\pm 0.1 mA$ | -                  | -   | 7.5             | ΚΩ   | *8   |

- \*1. CL, FRM, M, RSTB, CLK1, CLK2
- \*2. CS1B, CS2B, CS3, E, R/W, RS, DB0~DB7
- \*3. DB0~DB7
- \*4. Except DB0~DB7
- \*5. DB0~DB7 at High Impedance
- \*6. V0L(R), V2L(R), V3L(R), V5L(R)
- \*7. 1/64 duty, FCLK=250KHZ, Frame Frequency=70HZ, Output: No Load
- \*8. VDD~VEE=15.5V

 $V0L(R) > V2L(R) = V_{DD} - 2/7 \ (V_{DD} - V_{EE}) > V3L(R) = V_{EE} + 2/7 (V_{DD} - V_{EE}) > V5L(R)$ 



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### 9. Reset Timing

The system can be initialized by setting RSTB terminal at low level when turning power on, receiving instruction from MPU. When RSTB becomes low, following procedure is occurred.

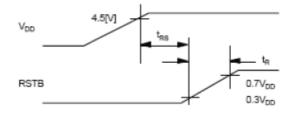
- 1 Display off
- 2. Display start line register become set by 0.(Z-address 0)

While RSTB is low, No instruction except status read can be accepted. Therefore, execute other instructions after making sure that DB4=0 (clear RSTB) and DB7=0 (ready) by status read instruction.

The Conditions of power supply at initial power up are shown in table 1.

Table 1. Power Supply Initial Conditions

| Item       | Symbol          | Min | Тур | Max | Unit |
|------------|-----------------|-----|-----|-----|------|
| Reset Time | t <sub>RS</sub> | 1.0 | -   | -   | us   |
| Rise Time  | t <sub>R</sub>  | -   | -   | 200 | ns   |





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## 10. CONTROL AND DISPLAY INSTRUCTION

The display control instructions control the internal state of the KS0108B. Instruction is received from MPU to KS0108B for the display control. The following table shows various instructions.

| Instruction                          | RS | R/W | DB7              | DB6 | DB5         | DB4       | DB3    | DB2        | DB1           | DB0 | Function   |
|--------------------------------------|----|-----|------------------|-----|-------------|-----------|--------|------------|---------------|-----|--|
| Display ON/OFF                       | L  | L   | L                | L   | H           | Н         | Н      | Н          | Н             | L/H | Controls the display on<br>or off. Internal status<br>and display RAM data is<br>not affected.<br>L:OFF, H:ON    |
| Set Address<br>(Y address)           | L  | L   |                  | Н   |             | Υa        | ddress | (0~63)     | •             |     | Sets the Y address in<br>the Y address counter.  |
| Set Page<br>( X address)             | L  | L   | Τ                | L   | Η           | Н         | Н      |            | Page<br>(0~7) |     | Sets the X address at the X address register.  |
| Display Start<br>Line<br>(Z address) | L  | L   | Н                | Н   |             |           |        | start line | 9             |     | Indicates the display data RAM displayed at the top of the screen.   |
| Status Read                          | L  | Н   | B<br>U<br>S<br>Y | L   | O N / O F F | R E S E T | L      | L          | L             | L   | Read status.  BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset         |
| Write Display<br>Data                | Н  | L   |                  |     |             | Write D   | ata    |            |               |     | Writes data (DB0:7) into display data RAM. After writing instruction, Y address is increased by 1 automatically. |
| Read Display<br>Data                 | Н  | Н   |                  |     |             | Read D    | )ata   |            |               |     | Reads data (DB0:7) from display data RAM to the data bus.  |

## 11. BACK LIGHT CHARACTERISTICS

LCD Module with EL Backlight **ELECTRICAL RATINGS** 

Drive voltage: AC50V,200Hz

Color: white

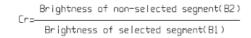


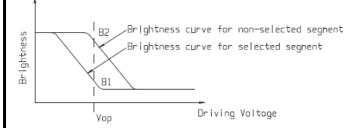
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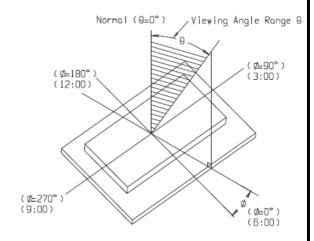
### 12. ELECTRO-OPTICAL CHARACTERISTICS

(  $V_{\text{OP}} = 9.0 \text{V}$ ,  $Ta = 25^{\circ}\text{C}$  )

| Item                 | Symbol | Condition           | Min | Тур | Max | Unit |
|----------------------|--------|---------------------|-----|-----|-----|------|
|                      |        | $Ta = -10^{\circ}C$ | 9.1 | 9.3 | 9.5 |      |
| Operating Voltage    | Vop    | $Ta = 25^{\circ}C$  | 8.8 | 9.0 | 9.2 | V    |
|                      |        | $Ta = 60^{\circ}C$  | 8.5 | 8.7 | 8.9 |      |
| Response time        | Tr     | Ta = 25°C           |     | 185 |     | ms   |
| Response time        | Tf     | 1a – 25 C           |     | 200 |     | ms   |
| Contrast             | Cr     | $Ta = 25^{\circ}C$  |     | 4   |     |      |
| Viouving angle ronge | θ      | Cr≥2                | -40 |     | +40 | deg  |
| Viewing angle range  | Φ      | CI = 2              | -40 |     | +40 | deg  |









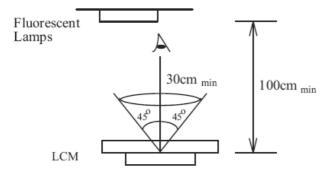
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# **13.QUALITY SPECIFICATIONS**

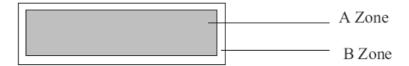
#### 13.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).



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# 13.2 Specification of quality assurance

AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: \* is not including)

| Classify |               | Item                         | Note | AQL  |
|----------|---------------|------------------------------|------|------|
| Major    | Display state | Short or open circuit        | 1    | 0.65 |
|          |               | LC leakage                   |      |      |
|          |               | Flickering                   |      |      |
|          |               | No display                   |      |      |
|          |               | Wrong viewing direction      |      |      |
|          |               | Contrast defect (dim, ghost) | 2    |      |
|          |               | Back-light                   | 1,8  |      |
|          | Non-display   | Flat cable or pin reverse    | 10   |      |
|          |               | Wrong or missing component   | 11   |      |
| Minor    | Display       | Background color deviation   | 2    | 1.0  |
|          | state         | Black spot and dust          | 3    |      |
|          |               | Line defect, Scratch         | 4    |      |
|          |               | Rainbow                      | 5    |      |
|          |               | Chip                         | 6    |      |
|          |               | Pin hole                     | 7    |      |
|          |               | Protruded                    | 12   |      |
|          | Polarizer     | Bubble and foreign material  | 3    |      |
|          | Soldering     | Poor connection              | 9    |      |
|          | Wire          | Poor connection              | 10   |      |
|          | TAB           | Position, Bonding strength   | 13   |      |



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| No. | Item   |  |       | Criterion   |   |
|-----|--|--|-------|---|---|
| 1   | Short or open circuit  |  |       | Not allow   |   |
|     | LC leakage   | 1  |       |   |   |
|     | Flickering   | -  |       |   |   |
|     | No display   |  |       |   |   |
|     | Wrong viewing direction  |  |       |   |   |
|     | Wrong Back-light   |  |       |   |   |
| 2   | Contrast defect  |  | Refe  | er to approval san  | nple  |
|     | Background color deviation   |  |       |   |   |
| 3   | Point defect,<br>Black spot, dust<br>(including Polarizer)<br>$\phi = (X+Y)/2$ | $\frac{1}{ \widehat{X} }$  | Uni   | Point<br>Size<br>$\phi \leq 0.10$<br>$0.10 < \phi \leq 0.20$<br>$0.20 < \phi \leq 0.25$<br>$0.25 < \phi \leq 0.30$<br>$\phi > 0.30$<br>it: mm | Acceptable Qty.  Disregard  3  2  1  0                    |
| 4   | Line defect, Scratch   | $ \begin{array}{c}  & \downarrow \\  & \uparrow \\  & \downarrow \\  $ | L<br> | EL 0.03≥W<br>EL 0.05≥W<br>EL 0.1>W<br>0.05 <w< td=""><td>Acceptable Qty.  Disregard  2  1  Applied as point defect</td></w<>                  | Acceptable Qty.  Disregard  2  1  Applied as point defect |
| 5   | Rainbow  | Not more than tw   |       | Unit: mm  | the viewing area.   |



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| No | Item  | Criterion   |
|----|---|---|
| 6  | Chip  Remark:  X: Length  direction   | Acceptable criterion $\begin{array}{c ccccccccccccccccccccccccccccccccccc$  |
|    | Y: Short direction  Z: Thickness direction  t: Glass thickness  W: Terminal Width | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$                       |
|    |   | Acceptable criterion $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ |
|    |   | Acceptable criterion $\begin{array}{c ccccccccccccccccccccccccccccccccccc$  |
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|-------|--|--|
| No. 7 | Segment pattern $W = Segment \ width$ $\phi = (X+Y)/2$ | Criterion  (1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ Point Size Acceptable Qty $\phi \le 1/4 \text{W} \qquad \text{Disregard}$ $1/4 \text{W} < \phi \le 1/2 \text{W} \qquad 1$ $\phi > 1/2 \text{W} \qquad 0$ Unit: mm   |
| 8     | Back-light   | <ul><li>(1) The color of backlight should correspond its specification.</li><li>(2) Not allow flickering</li></ul>   |
| 9     | Soldering  | (1) Not allow heavy dirty and solder ball on PCB.  (The size of dirty refer to point and dust defect)  (2) Over 50% of lead should be soldered on Land.  Lead  Land  50% lead  |
| 10    | Wire PCB   | <ol> <li>(1) Copper wire should not be rusted</li> <li>(2) Not allow crack on copper wire connection.</li> <li>(3) Not allow reversing the position of the flat cable.</li> <li>(4) Not allow exposed copper wire inside the flat cable.</li> <li>(1) Not allow screw rust or damage.</li> <li>(2) Not allow missing or wrong putting of component.</li> </ol> |



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|    | I                              |   |  |
|----|--------------------------------|---|--|
| No | Item                           | Criterion   |  |
| 12 | Protruded W: Terminal Width    | Acceptable criteria: $Y \le 0.4$  |  |
| 13 | TAB                            | 1. Position  W W1 ≤ 1/3W H1 ≤ 1/3H  2 TAB bonding strength test  TAB  P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)   |  |
| 14 | Total no. of acceptable Defect | A. Zone  Maximum 2 minor non-conformities per one unit.  Defect distance: each point to be separated over 10mm  B. Zone  It is acceptable when it is no trouble for quality and assembly in customer's end product. |  |



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#### 13.3 Reliability of LCM

Reliability test condition:

| Item                 | Condition  | Time (hrs) | Assessment       |
|----------------------|--|------------|------------------|
| High temp. Storage   | 70°C   | 48         |                  |
| High temp. Operating | 60°C   | 48         |                  |
| Low temp. Storage    | -20°C  | 48         | No abnormalities |
| Low temp. Operating  | -10°C  | 48         | in functions     |
| Humidity             | 40°C/90%RH   | 48         | and appearance   |
| Temp. Cycle          | $0^{\circ}\text{C} \leftarrow 25^{\circ}\text{C} \rightarrow 50^{\circ}\text{C}$ | 10cycles   |                  |
|                      | $(30 \min \leftarrow 5 \min \rightarrow 30 \min)$                                |            |                  |

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $20\pm8^{\circ}$ C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

#### 13.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

#### **General Precautions:**

- LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not make any modification on the PCB without consulting P-tec.
- When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.



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7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

#### Static Electricity Precautions:

- CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

#### **Soldering Precautions:**

- Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C±10°C
- Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

#### **Operation Precautions:**

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

#### **Limited Warranty**

P-tec LCDs and modules are not consumer products, but may be incorporated by P-tec customers into consumer products or components thereof, P-tec does not warrant that its LCDs and components are fit for any such particular purpose.



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- The liability of P-tec is limited to repair or replacement on the terms set forth below.
   P-tec will not be responsible for any subsequent or consequential events or injury or
   damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed
   in writing between P-tec and the customer, P-tec will only replace or repair any of
   its LCD which is found defective electrically or visually when inspected in accordance with P-tec
   general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.