



PRODUCT SPECIFICATION

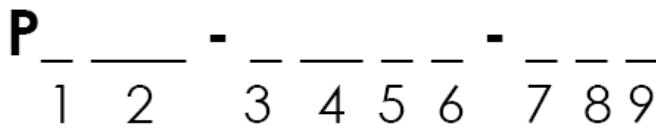
Part Number

PG16064A-O Series

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	
APPROVED BY	
DATE	



1. Part number breakdown



Replace each Space (_) with the following letters and or numbers

1. P-tec LCD Type	C = Character G = Graphic COG = Chip On Glass	COF = Chip On Flex TAB = Tape Automated Bonding TFT = Thin-film Transistor
2. LCD Model	Example for Character: 2002A = 20 Characters x 2 Lines w/ Pins on Left side and 116mm x 37 x 12.7mm overall size Example for Graphic: 12864B = 128 Dots per row x 64 Dots per Column w/ Pins on lower side and 93mm x 70 x 8.8mm overall size	
3. Fluid Type	T = TN/Grey Y = STN/Yellow Green G = STN/ Grey	B = STN/ Blue F = FSTN/ White N = FSTN/ Black
4. Backlight/polorizer	NF = None/Transflective NM = None/Transmissive NR = None/Reflective EF = EL/Transflective EM = EL/Transmissive	LF = LED/Transflective LM = LED/Transmissive CF = CCFL/Transflective CM = CCFL=Transmissive
5. Backlight Color	(If no backlight provided move on to viewing angle [6.]) B = Blue/Green Y = Yellow G = Green	
6. Viewing Angle	D = 6:00 U = 12:00	R = 3:00 L = 9:00
7. Internal Number	Single Letter for internal purposes	
8. Extended Temperature	This space is blank if operating temperature is standard 0°C to 50°C An X will be visible if the LCD is Extended operating temperature	
9. Customer Specials or List of Value-added items	Usually blank unless customer requests some modifications. Can be several Letters long.	

2. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.

3. General Specification

Item	Dimension	Unit
Number of Dots	160 x 64	—
Module dimension(No Backlight)	89.0 x 35.6 x 17.0 (MAX)	mm
Module dimension(With LED Backlight)	89.0 x 35.6 x 17.0 (MAX)	mm
View area	62.4 x 27.6	mm
Active area	59.17 x23.65	mm
Dot size	0.34 x 0.34	mm
Dot pitch	0.37 x 0.37	mm
LCD type	STN, Yellow Green	
Driving Method	1/64 Duty	
View direction	12 o'clock	
Backlight Type	YELLOW-GREEN backlight	

4. Absolute Maximum Ratings

Item		Symbol	Min	Max	Unit
Input Voltage		V_I	-0.3	$V_{DD}+0.3$	V
Supply Voltage For Logic		$V_{DD}-V_{SS}$	-0.3	7.0	V
Supply Voltage For LCD		$V_{DD}-V_0$	$V_{dd}-13.5$	0	V
Standard Temperature LCM	Operating Temp.	Top	0	50	°C
	Storage Temp.	Tstr	-10	60	°C
Wide Temperature LCM	Operating Temp.	Top	-20	70	°C
	Storage Temp.	Tstr	-30	80	°C

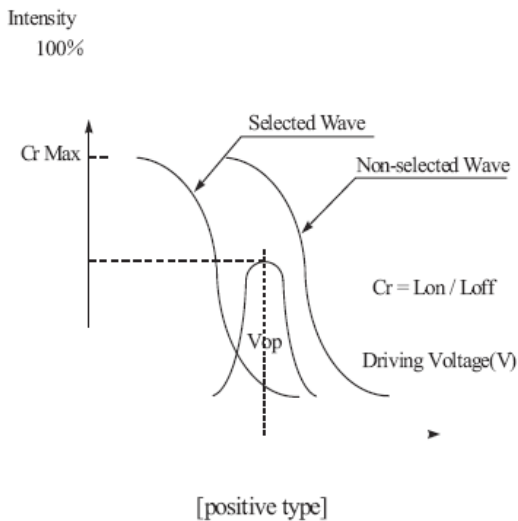
5. Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.5	5.0	5.5	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=25^{\circ}\text{C}$	13.0	14.4	15.0	V
Input High Volt.	V_{IH}	—	$0.7 V_{DD}$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	V_{SS}	—	$0.3 V_{DD}$	V
Supply Current	I_{DD}	$V_{DD}=5\text{V}$	-	-	8.0	mA
Supply Voltage of Yellow-green backlight	V_{LED}	Forward current =120mA Number of LED die 2x14= 24	3.8	4.1	4.3	V

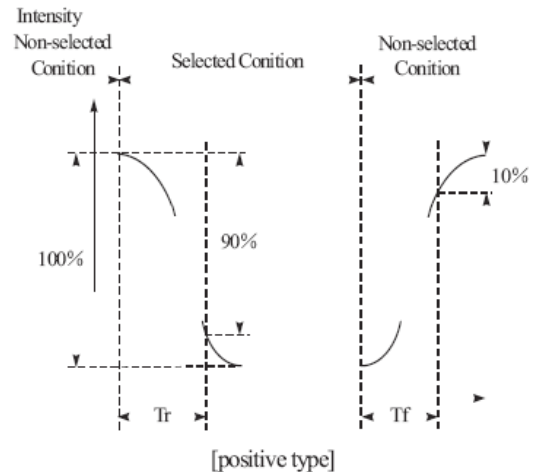
6. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	(V) θ	$CR \geq 2$	-20	—	35	deg
	(H) ϕ	$CR \geq 2$	-30	—	30	deg
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	—	250	ms
	T fall	—	—	—	250	ms

Definition of Operation Voltage (Vop)



Definition of Response Time (Tr, Tf)



Conditions :

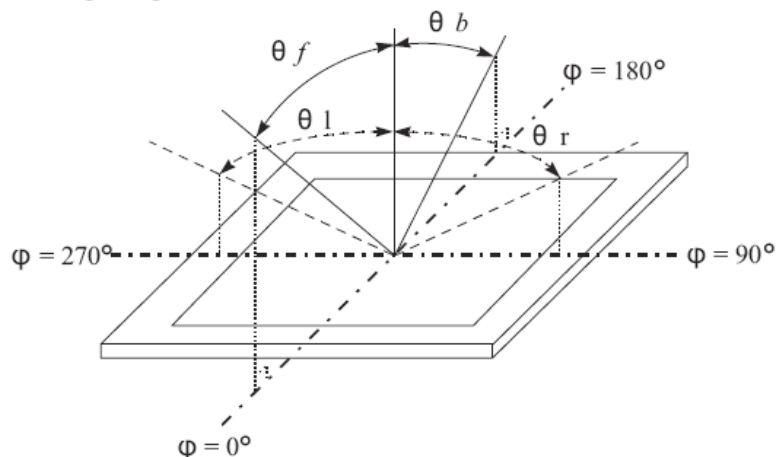
Operating Voltage : Vop

 Viewing Angle(θ , ϕ) : 0° , 0°

Frame Frequency : 64 HZ

Driving Waveform : 1/N duty, 1/a bias

Definition of viewing angle($CR \geq 2$)

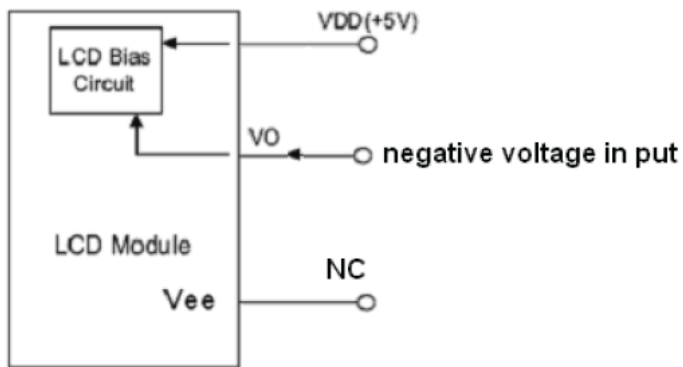


7. Interface Pin Function

Pin No.	Symbol	Level	Description
1	FLM	H	Frame signal
2	CL1	H/L	Data latch pulse
3	CL2	H/L	Data shift pulse
4	M	H/L	Alternate signal for LCD drive
5	V0		Operating voltage for LCD
6	VCC	5.0V	Supply voltage for LCD drive(+)
7	VSS	0V	Ground
8	VEE		Negative voltage output
9	D1	H/L	Data bit 1
10	D2	H/L	Data bit 2
11	D3	H/L	Data bit 3
12	D4	H/L	Data bit 4
13	/DISO OFF	H/L	H: Display on, L: Display off
13	DB6	H/L	Data bit 6
14	A		Power supply for LED (+)
15	K		Power supply for LED (-)

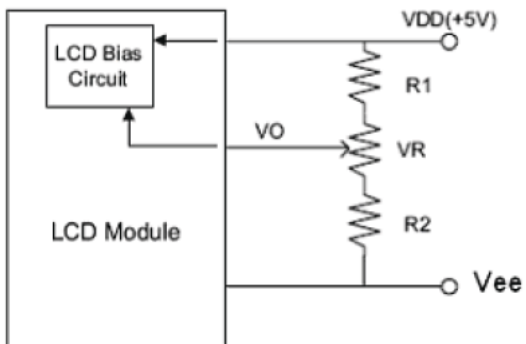
8. POWER SUPPLY

Without Negative Power on PCB



without DC-DC converter

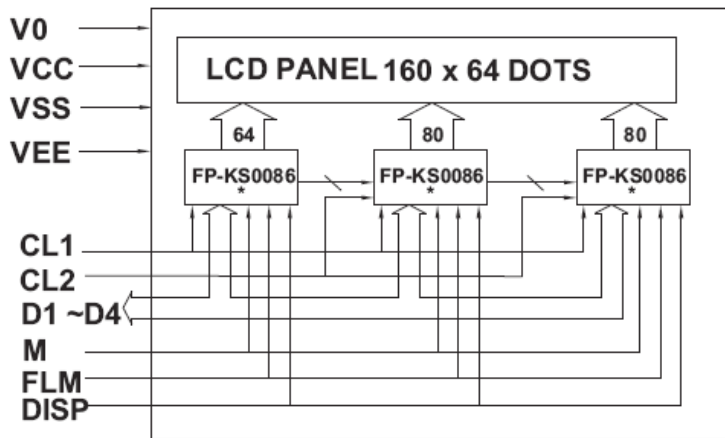
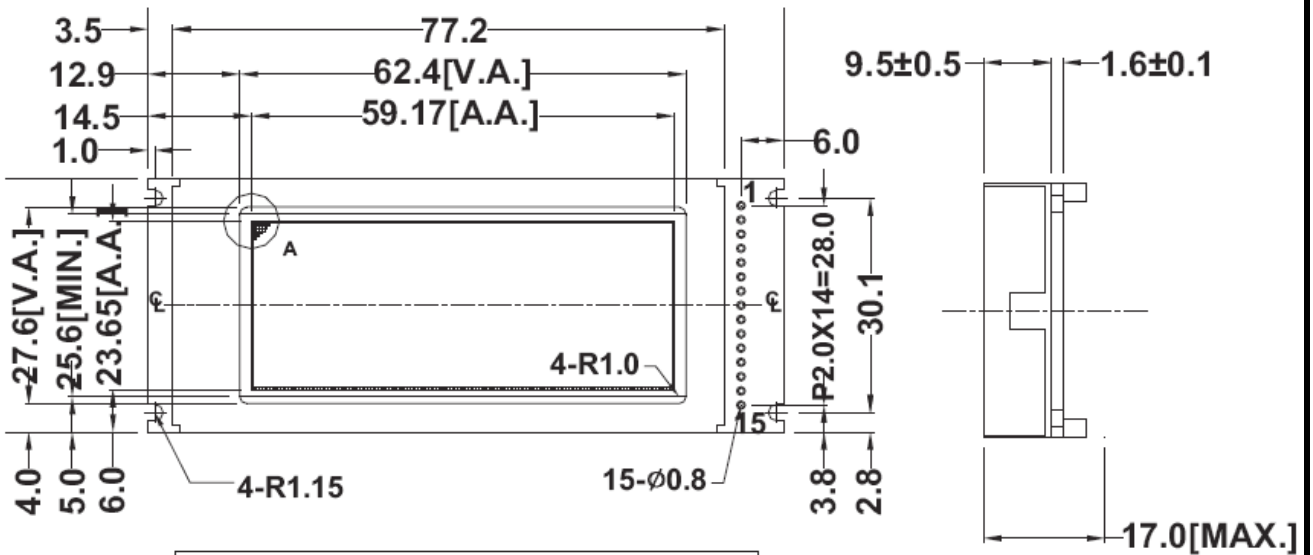
With Negative Power on PCB



with DC-DC converter

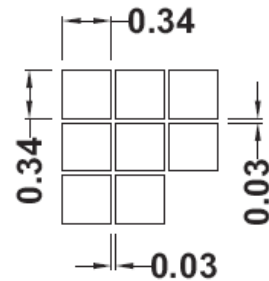
VR:10K-20K

9. Contour Drawing & Block Diagram



*OR EQUIVALENT

DEAIL A:

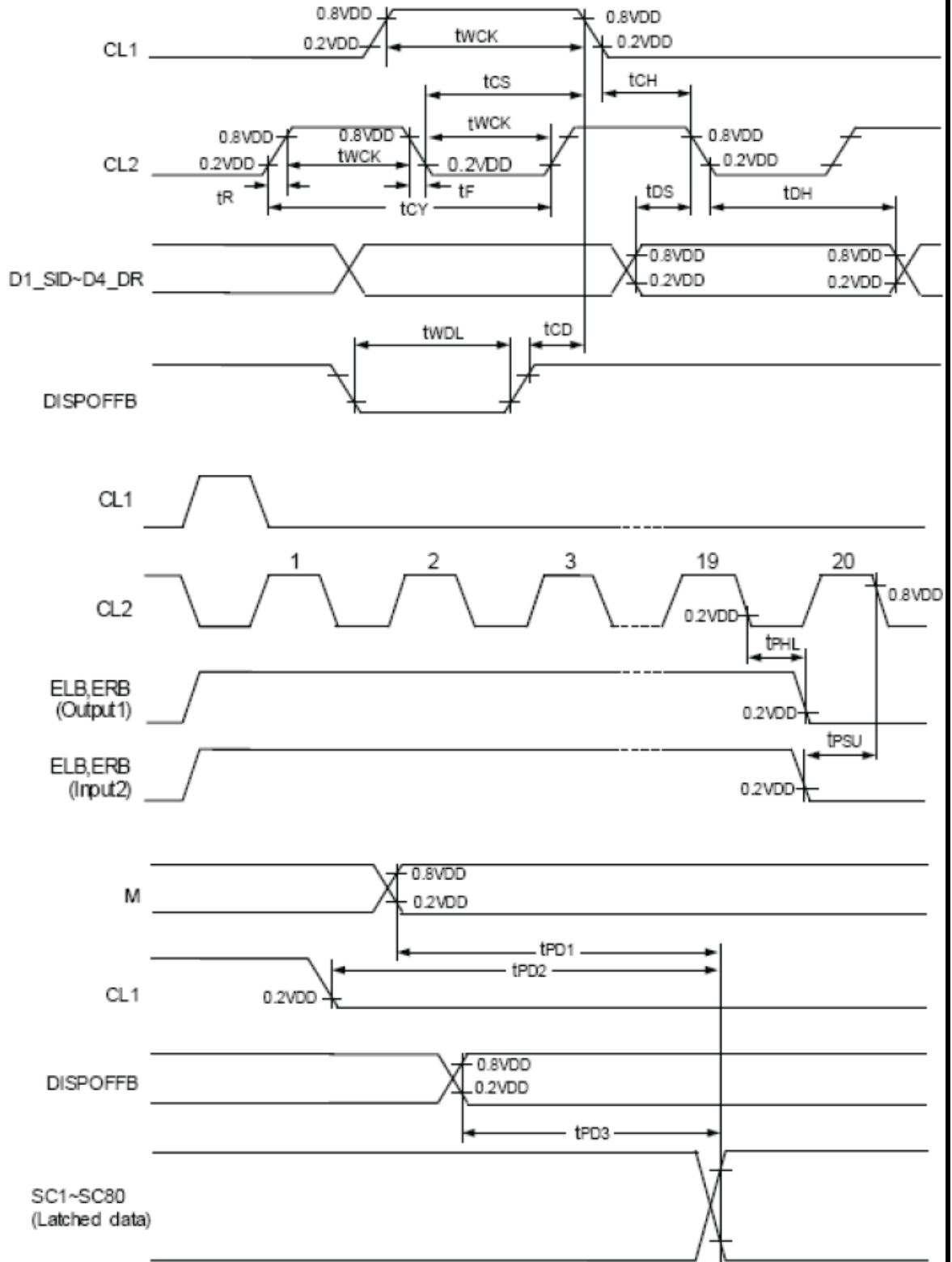


10. Timing Characteristics

(1) SEGMENT DRIVER APPLICATION

 (V_{SS} = 0 V, T_a = -30 ~ +85°C)

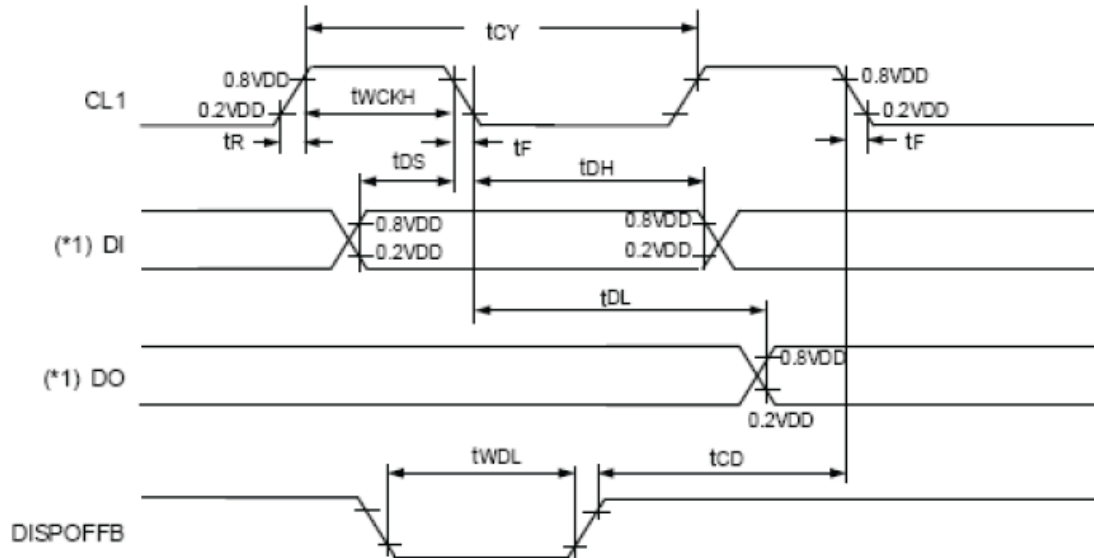
Characteristic	Symbol	Test Condition	(1) VDD=5 V ± 10%			(2) VDD=3 V ± 10%			Unit
			MIN	TYP	MAX	MIN	TYP	MAX	
Clock cycle time	t _{CY}	Duty=50%	125	-	-	250	-	-	ns
Clock pulse width	t _{WCK}	-	45	-	-	95	-	-	
Clock rise/fall time	t _{R/F}	-	-	-	-	-	-	30	
Data set-up time	t _{DS}	-	30	-	-	65	-	-	
Data hold time	t _{DH}	-	30	-	-	65	-	-	
Clock set-up time	t _{CS}	-	80	-	-	120	-	-	
Clock hold time	t _{CH}	-	80	-	-	120	-	-	
Propagation delay time	t _{PHL}	ELB Output	-	-	60	-	-	125	
		ERB Output	-	-	60	-	-	125	
ELB,ERB set-up time	t _{PSU}	ELB Input	30	-	-	65	-	-	
		ERB Input	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{WDL}	-	1.2	-	-	1.2	-	-	μs
DISPOFFB clear time	t _{CD}	-	100	-	-	100	-	-	ns
M - OUT propagation delay time	t _{PD1}	CL=15 pF	-	-	1.0	-	-	1.2	μs
CL1 - OUT propagation delay time	t _{PD2}		-	-	1.0	-	-	1.2	
DISPOFFB - OUT propagation delay time	t _{PD3}		-	-	1.0	-	-	-	

SEGMENT DRIVER APPLICATION TIMING


(2) COMMON DRIVER APPLICATION

 (V_{SS} = 0 V, T_a = -30 ~ +85°C)

Characteristic	Symbol	Test Condition	(1) VDD=5 V ± 10%			(2) VDD=3V±10%			Unit
			MIN	TYP	MAX	MIN	TYP	MAX	
Clock cycle time	t _{CY}	Duty=50%	250	-	-	500	-	-	ns
Clock pulse width	t _{WCK}	-	45	-	-	95	-	-	
Clock rise/fall time	t _{R/F}	-	-	-	50	-	-	50	
Data set-up time	t _{DS}	-	30	-	-	65	-	-	
Data hold time	t _{DH}	-	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{WDL}	-	1.2	-	-	1.2	-	-	μs
DISPOFFB clear time	t _{CD}	-	100	-	-	100	-	-	ns
Output delay time	t _{DL}	CL=15 pF	-	-	200	-	-	250	μs
M - OUT propagation delay time	t _{PD1}		-	-	1.0	-	-	1.2	
CL1 - OUT propagation delay time	t _{PD2}		-	-	1.0	-	-	1.2	
DISPOFFB - OUT propagation delay time	t _{PD3}		-	-	1.0	-	-	1.2	

COMMON DRIVER APPLICATION TIMING


(*1) When in single-type interface mode

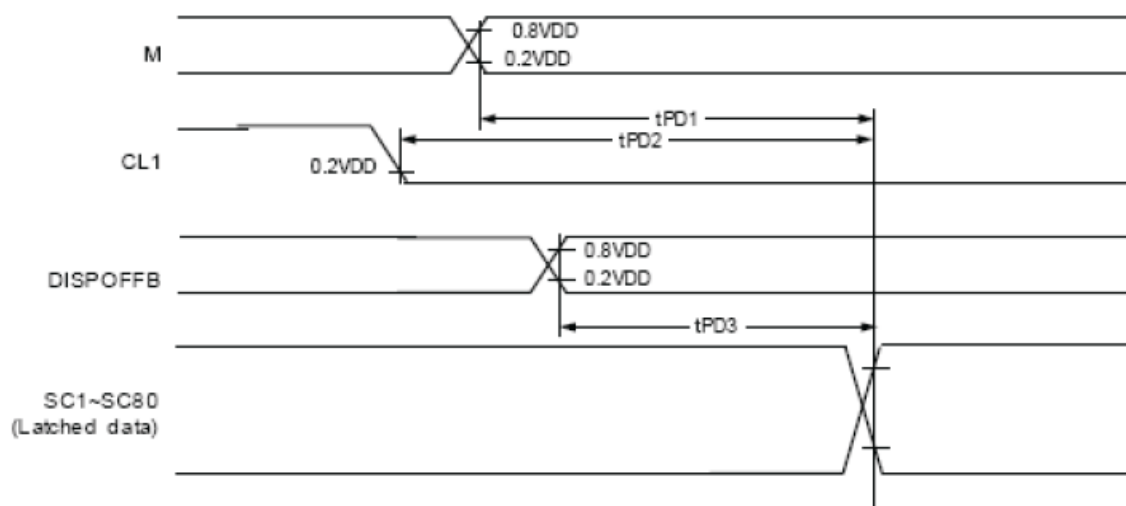
DI \Rightarrow D2_DL(SHL="L"), D4_DR(SHL="H")

DO \Rightarrow D4_DR(SHL="L"), D2_DL(SHL="H")

When in dual-type interface mode

DI \Rightarrow D2_DL and D3_DM(SHL="L"), D4_DR and D3_DM(SHL="H")

DO \Rightarrow D4_DR(SHL="L"), D2_DL(SHL="H")



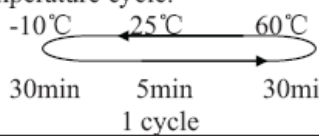
11. Quality Assurance

Screen Cosmetic Criteria

Item	Defect	Judgment Criterion	Partition
1	Spots	A)Clear <u>Size: d mm</u> <u>Acceptable Qty in active area</u> $d \leq 0.1$ Disregard $0.1 < d \leq 0.2$ 6 $0.2 < d \leq 0.3$ 2 $0.3 < d$ 0 Note: Including pin holes and defective dots which must be within one pixel size. B)Unclear <u>Size: d mm</u> <u>Acceptable Qty in active area</u> $d \leq 0.2$ Disregard $0.2 < d \leq 0.5$ 6 $0.5 < d \leq 0.7$ 2 $0.7 < d$ 0	Minor
2	Bubbles in Polarizer	<u>Size: d mm</u> <u>Acceptable Qty in active area</u> $d \leq 0.3$ Disregard $0.3 < d \leq 1.0$ 3 $1.0 < d \leq 1.5$ 1 $1.5 < d$ 0	Minor
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor
5	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-light type should be judged with back-light on state only.	Minor

12. Reliability

Content of Reliability Test

Environmental Test			
Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Endurance test applying the high storage temperature for a long time.	60°C 96hrs	—
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-10°C 96hrs	—
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50°C 96hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	0°C 96hrs	—
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 96hrs	—
High Temperature/ Humidity Operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	50°C,90%RH 96hrs	—
Temperature Cycle	Endurance test applying the low and high temperature cycle. 	-10°C/60°C 10 cycles	—
Mechanical Test			
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	—
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msdc 3 times of each direction	—

***Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25 °C