

PRODUCT SPECIFICATION

Part Number

PG20232A-O Series

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	
APPROVED BY	
DATE	



MODEL NO.

No.	Contents	Page
1	Part number breakdown	4
2	Precautions	5
3	General Specification	5
4	Absolute Maximum Ratings	5
5	Electrical Characteristics	6
6	Optical Characteristic	6
7	Pin description	7
8	Power supply	8
9	Contour Drawing & Block Diagram	9
10	Timing Characteristics	10
11	Instruction Table	11
12	Quality Assurance	12
13	Reliability	13



MODEL NO.

PAGE

Rev.	Comments	Page	Date
1	Preliminary Specification was first issued.	All	8/8'14

n	MODEL	2 NO.	PAGE
P-TEC	PG20232A-O series	SPEC ONLY	4
1 <u>. Part number</u> P 1 2 3 4	breakdown		
Replace each Space (_ ,) with the following letters a	nd or numbers	
1. P-tec LCD Type	G = Graphic TAB	F = Chip On Flex = Tape Automated Bor = Thin-film Transistor	nding
2. LCD Model	Example for Character: 2002A Example for Graphic: 12864B	side and 116mm x 37 overall size	⁷ x 12.7mm Dots per Column
3. Fluid Type	Y = STN/Yellow Green F =	= STN/ Blue = FSTN/ White = FSTN/ Black	
4. Backlight/polorizer	NM= None/Transmissive LA NR=None/Reflective CI	= LED/Transflective 1 = LED/Transmissive 5 = CCFL/Transflective M =CCFL=Transmissive	
5. Backlight Color	$\mathbf{Y} = \text{Yellow}$ O	e on to viewing angle [= Yellow/Green = Orange = White	[6.])
6. Viewing Angle		= 3:00 = 9:00	
7. Internal Number	Single Letter for internal purpo	ses	
8. Extended Temperature	This space is blank if operating An X will be visible if the LCD is		
9. Customer Specials or List of Value-added items	Usually blank unless customer Can be several Letters long.	requests some modifica	ations.



2.Precautions in use of LCD Modules

- (1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6)Soldering: only to the I/O terminals.

(7)Storage: please storage in anti-static electricity container and clean environment.

3.General Specification

Item	Dimension	Unit
Number of Characters	202 x 32	-
Module dimension	150.0 x 45.8 x 12.0 (MAX)	mm
View area	123.0 x 24.0	mm
Active area	119.14 x 21.08	mm
Dot size	0.55 x 0.62	mm
Dot pitch	0.59 x 0.66	mm
LCD type	STN,Positive	
Duty	1/32	
View direction	6 o'clock	
Backlight Type	WHITE LED BACKLIGHT	

4.Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	Т _{ОР}	-0		+50	°C
Storage Temperature	T _{ST}	-10	_	+60	°C
Input Voltage	V_{I}	-0.3		VDD+0.3	V
Supply Voltage For Logic	V_{DD} - V_{SS}	-0.3		7.0	V
Supply Voltage For LCD	V_{DD} - V_0	Vdd-13.5	—	0	V



PG20232A-O series	

5. Electrical Characteristics

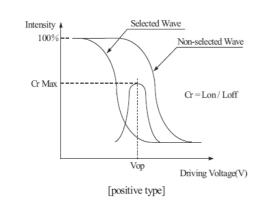
Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V_{DD} - V_{SS}	-	4.5	5.0	5.5	V
Supply Voltage For LCD	V _{DD} -V ₀	Ta=25℃	-	5.0	-	V
Input High Volt.	V_{IH}	_	$0.7 \ V_{DD}$	_	V _{DD}	V
Input Low Volt.	V_{IL}	_	V _{SS}		$0.3 V_{DD}$	V
Supply Current	I _{DD}	V _{DD} =5V	-	0.8	1.2	mA
Supply Voltage of White backlight	V _{LED}	Forward current =45 mA Number of LED dice 3	2.9	3.1	3.3	V

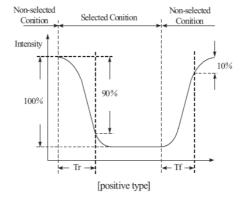
6.Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	$(V)\theta$	$CR \ge 2$	20	_	35	deg
view ringie	$(\mathrm{H})\varphi$	$CR \ge 2$	-30		30	deg
Contrast Ratio	CR	—		3		—
Response Time	T rise	—			250	ms
	T fall	_	_	_	250	ms

Definition of Operation Voltage (Vop)

Definition of Response Time (Tr , Tf)

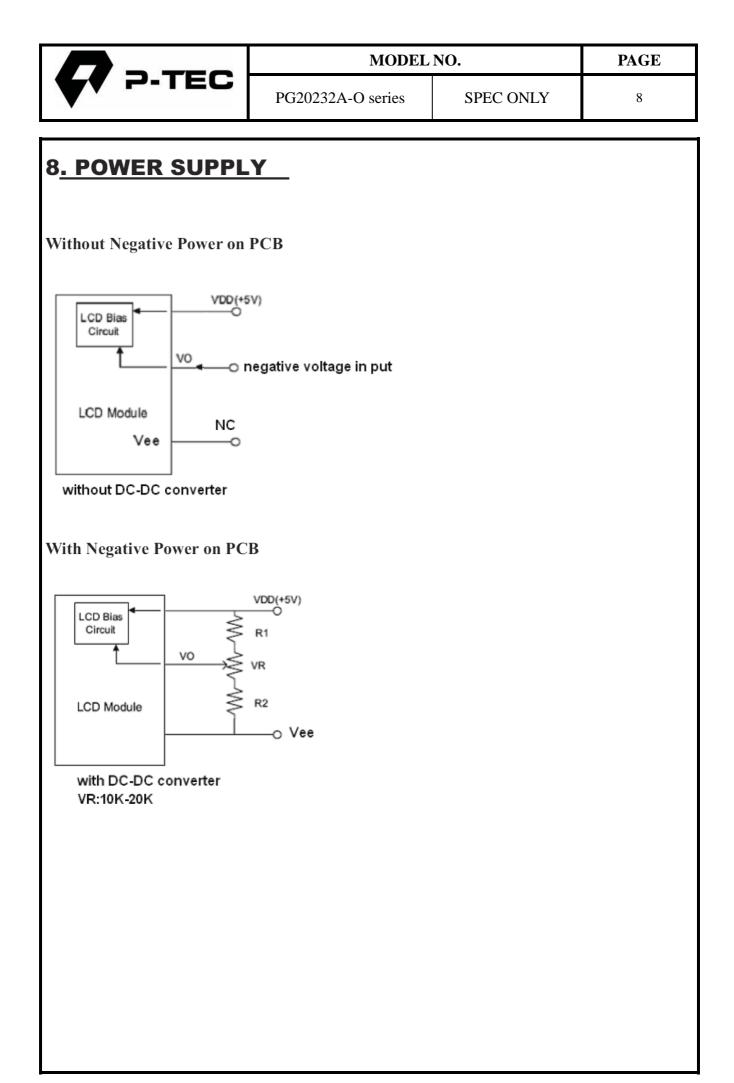




	MODEL	NO.	PAGE	
P-TEC	PG20232A-O series	32A-O series SPEC ONLY		
	Voltage : Vop Viewing Ang uency : 64 HZ Driving Wav	gle $(\theta \ , \ \varphi) : 0^\circ \ , \ 0^\circ$ reform : 1/N duty , 1/a bia	IS	
Definition of viewing angle	e(CR≧2)			
φ = 270°	θ f θ l	$\phi = 180^{\circ}$ θ r $\phi = 90^{\circ}$		

7.Interface Pin Function

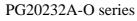
Pin No.	Symbol	Level	Description
1	Vss	0V	GND
2	VDD	5.0V	Power supply for LOGIC
3	V0		Operating voltage for LCD
4	A0	H/L	Register Select
5	R/W	H/L	Read/Write
6	CS1	H/L	Chip Select Signal
7~14	DB0~DB7	H/L	Data Bus
15	Vee		Negative Voltage for LCD
16	/RES	H/L	Set Chip Initialized
17	А	3.1V	Anode of Backlight
18	К	0V	Cathode of Backlight
19	CS2	H/L	Chip Select Signal
20	CS3	H/L	Chip Select Signal





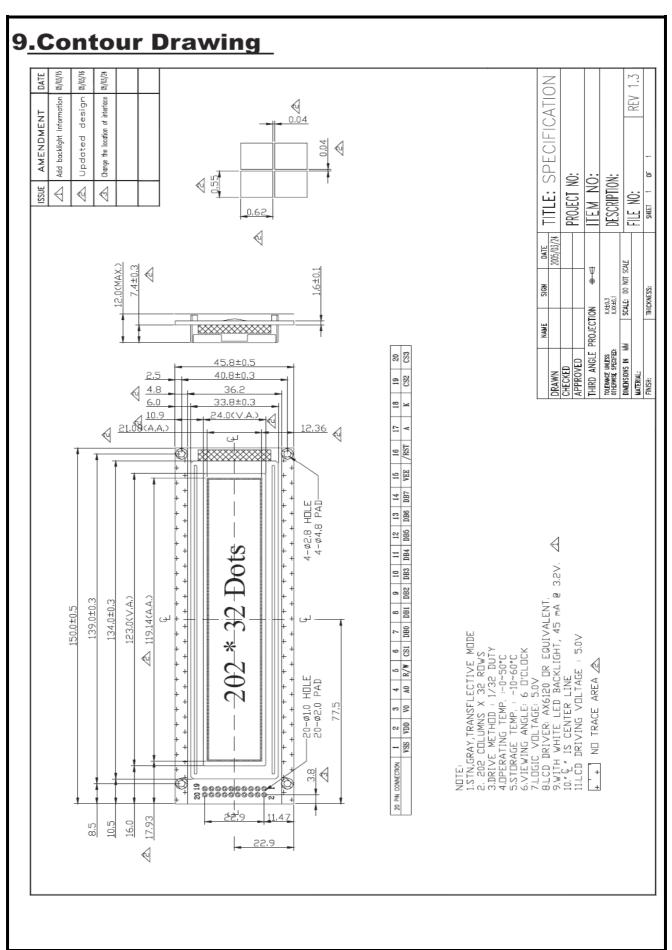
MODEL NO.

PAGE



SPEC ONLY

9



—			Μ	IODEL NO).		PAGE
••-	P-TEC	PC	620232A-O se	ries	SPEC ONI	Х	10
<u>10.Tim</u>	ing Ch	aract	eristic	S			
• MPU Bus I	Read/Write l	I (68-famil		ZC6			
-	е		t AW6	^t EW			
	/₩				t AH6	×	
DO) to D <u>7</u> RITE)			ACC6	^t DH6	<	
	to D7			— (
48 - 43 KT	AD)	=-5 0V + 1	0% unless sta	ted otherwi	ا جو		
48 - 43 KT	5 deg. C. Vss	<u>= -5.0V± 1</u> Symbol	0% unless stat		se ting	Unit	Signal
Ta= -20 to 75	5 deg. C. Vss					Unit	Signal
Ta= -20 to 75	5 deg. C. Vss [.] neter			Ra	ting	Unit	
Ta= -20 to 75 Paran	5 deg. C. Vss [.] neter e time	Symbol		Ra min	ting max		
Ta= -20 to 75 Paran System cycle	5 deg. C. Vss ³ neter e time ıp time	Symbol tCYC6		Ra min 1,000	ting max 	ns	
Ta= -20 to 75 Paran System cycle Address setu	5 deg. C. Vss neter e time 1p time d time	Symbol tCYC6 tAW6		Ra min 1,000 20	ting max 	ns ns	
Ta= -20 to 75 Paran System cycle Address setu Address hole	5 deg. C. Vss neter e time p time d time ime	Symbol tCYC6 tAW6 tAH6		Ra min 1,000 20 10	ting max 	ns ns ns	A0, CS , R/ W
Ta= -20 to 75 Paran System cycle Address setu Address hole Data setup ti	5 deg. C. Vss ³ neter e time up time d time ime ne	Symbol tCYC6 tAW6 tAH6 tDS6	Condition	min 1,000 20 10 80	ting max 	ns ns ns ns	
Ta= -20 to 75 Paran System cycle Address setu Address hole Data setup ti Data hold tir	o deg. C. Vss neter e time up time d time ime ne ole time	Symbol tCYC6 tAW6 tAH6 tDS6 tDH6		Ra min 1,000 20 10 80 10	ting max 	ns ns ns ns ns	A0, CS, R/W
Ta= -20 to 75 Paran System cycle Address setu Address holo Data setup ti Data hold tir Output disab	o deg. C. Vss neter e time up time d time ime ne ole time	Symbol tCYC6 tAW6 tAH6 tDS6 tDH6 tOH6	Condition	Ra min 1,000 20 10 80 10 10 10	ting max 60	ns ns ns ns ns ns	A0, CS, R/W

tCYC6 is the cycle time of CS. E=H. not the cycle time of E
Increase parameter values by 200% when Vss= -3.0V.
all inputs must have a rise and fall time of less than 15 ns.



SPEC ONLY

PG20232A-O series

PAGE

11.Instruction Table

COMMANDS

Summary

Summary													
		Code											
Command	A0	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0	Function	
Display On/Off	0	1	0	1	0	1	0	1	1	1	0/1	Turns display on or off. 1 : ON, 0 : OFF	
Display start line	0	1	0	1	1	0	Displa	ıy sta	rt add	lress (0 to 31)	Specifies RAM line corresponding to top line of display.	
Set page address	0	1	0	1	0	1	1	1	0	Page	e (0 to 3)	Sets display RAM page in page address register.	
Set column (segment) address	0	1	0	0		Colu	mn add	ress (0 to 7	79)		Sets display RAM column address in column address registser.	
Read status	0	0	1	Busy	ADC	ON/OFF	Reset	0	0	0	0	Reads the following status : BUSY 1 : Busy 0 : Ready ADC 1 : CW output 0 : CCW output ON/OFF 1 : Display off 0 : Display on RESET 1 : Being reset 0 : Normal	
Write display data	1	1	0			7	Vrite da	ita				Writes data from data bus into display RAM.	
Read display data	1	0	1		Read data Reads data from display RAM onto data bus.								
Select ADC	0	1	0	1	0	1	0	0	0	0	0/1	0 : CW output, 1 : CCW output	
Statis drive ON/OFF	0	1	0	1	0	1	0	0	1	0	0/1	Selects static driving operation. 1 : Static drive, 0 : Normal driving	
Select duty	0	1	0	1	0	1	0	1	0	0	0/1	Selects LCD duty cycle 1 : 1/32, 0 : 1/16	
Read-Modify -Write	0	1	0	1	1	1	0	0	0	0	0	Read-modify-write ON	
End	0	1	0	1	1	1	0	1	1	1	0	Read-modify-write OFF	
Reset	0	1	0	1	1	1	0	0	0	1	0	Software reset	



SPEC ONLY

PG20232A-O series

12.Quality Assurance

Screen Cosmetic Criteria

Item	Defect	Judgment Criterion	Partition
1	Spots	A)ClearAcceptable Qty in active area $d \leq 0.1$ Disregard $0.1 < d \leq 0.2$ 6 $0.2 < d \leq 0.3$ 2 $0.3 < d$ 0Note: Including pin holes and defective dots which must be within one pixel size.B)UnclearAcceptable Qty in active area $d \leq 0.2$ Disregard $0.2 < d \leq 0.5$ 6 $0.5 < d \leq 0.7$ 2 $0.7 < d$ 0	Minor
2	Bubbles in Polarizer		Minor
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor
5	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-light type should be judged with back-light on state only.	Minor

		MODE	L NO.	PAGE	
7 7.	TEC	PG20232A-O series	SPEC ONLY	13	
B.Relia Content of R Environmenta	eliability Te	st			
Test Item	Content of Te	est	Test Condition	Applicable Standard	
High Temperature storage		at applying the high storage for a long time.	60°C 96hrs		
Low Temperature storage		at applying the high storage for a long time.	-10℃ 96hrs		
High Temperature Operation	(Voltage & Cu	at applying the electric stress arrent) and the thermal stress t for a long time.	50°C 96hrs		
Low Temperature Operation		at applying the electric stress apperature for a long time.	0°C 96hrs		
High Temperature/ Humidity Storage		at applying the high nd high humidity storage for a	60℃,90%RH 96hrs		
High Temperature/ Humidity Operation	(Voltage & Cu	at applying the electric stress aurrent) and temperature / ss to the element for a long	50℃,90%RH 96hrs		
Temperature Cycle	Endurance test temperature c -10°C 30min		-10°C/60°C 10 cycles		
Mechanical Tes	t				
Vibration test		at applying the vibration ortation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs		
Shock test		l and mechanical endurance the shock during	50G Half sign wave 11 msedc 3 times of each direction		

***Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25° C