

# **PRODUCT SPECIFICATION**

# Part Number PT322435B-TLMWD-E27

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	
APPROVED BY	
DATE	



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# 2. Record of Revisions

Rev.	Comments	Page	Date
1	Preliminary Specification was first issued	All	5/21'09
2	Modify 17. Outline Drawing	24	6/30'09



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### 3. Module Numbering System

PT\_\_\_\_--------

1. 2. 3. 4. 5. 6. 7. 8. 9 10. 11. 12. 13. 14.

### 1. P-TEC TFT

# 8. VIEWING DIRECTION D: 6 o'clock

D: 6 o'clock U: 12 o'clock F: Full Viewing Angle

# 2. LENGTH x WIDTH PIXELS

If third character is a zero, it is removed to shorten part number. Example: 240 x 320 = PT3224

### 9. A ~ Z CODE

Assigned by P-tec

#### 3. DIAGONAL DIMENSIONS

Example: 3.5" display = 35 in part number

#### 11. TEMPERATURE RANGE

Normal: Left Blank Wide: X

#### 4. PRODUCT VERSION

Series assigned by P-tec

#### 12. LUMINANCE

Blank: Normal (<300 nit) M: Middle (>/= 300 nit) H: High (> 600 nit)

### 5. LCD MODE

T: TN I: IPS V: VA

#### 13. TOUCH PANEL OPTION

14. SPECIAL CHARACTERS

No TP: Left Blank C: Capacitive TP R: Resistive TP

#### 6. POLARIZER

LM: Transmissive LF: Transflective

### Customer special requirements

7. BACKLIGHT COLOR

No Backlight: Left Blank W: White

B: Blue/Green
S: Yellow/Green

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### 4. Application

This specification is applied to the 3.5 inch QVGA supported TFT-LCD module, and can display 262k colors. The module is designed for PMP, GPS, DMB, other electronic products which require flat panel display of digital signal interface.

#### 5. Features

- QVGA (320×240 pixels) resolution.
- Digital 24 bit parallel RGB
- Serial Peripheral Interface (SPI).
- Line inversion mode with stripe type.
- On-chip voltage generator
- SYNC mode is supported for digital RGB input data format.

### 6. General Specifications

Item	Specifications	Unit
Screen Size	3.5 (Diagonal)	inch
Display Format	320RGB(H)×240(V)	dot
Active Area	70.08(H)×52.56(V)	mm
Dot Pitch	0.073(H)×0.219(V)	mm
Pixel Configuration	RGB Vertical Stripe	-
	TN Type	
Display Mode	Transmissive Mode	-
	Normally White	
Surface Treatment	Anti-Glare and Hard Coating(3H)	-
	6 O'clock	
Viewing Direction	(The Gray Inversion will appear at this direction)	-
Outline Dimension	76.9(W)×63.9(H)×3.3(D)	mm
DC to DC circuit	Build-in	-
Weight	(32.5)	g
RoHS Compliance	P-tec certifies this product to be in compliance with European Union Directive 2002/95/EC on the restriction of certain hazardous substances in electrical	-
	and electronic equipment.	



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### 7. Absolute Maximum Ratings

### 7.1 Absolute Ratings of Environment

Itam	Cranala a 1	Value		T T:4	NI - 4 -
Item	Symbol	Min.	Max.	Unit	Note
Storage Temperature	$T_{ST}$	-30	+80	°C	(1)
Operating Ambient Temperature	Top	-20	+70	°C	(1)

Note (1) Temperature and relative humidity range are shown in the figure below.

- (a) 90%RH Max. (Ta≦40°C).
- (b) Wet-bulb temperature should be 39°C Max. (Ta>40°C).
- (c) No condensation.

### 7.2 Electrical Absolute Ratings

#### 7.2.1 TFT-LCD Module

 $(Ta=25\pm2^{\circ}C, GND=V_{SS}=0V)$ 

Itom	Symbol	Value		Unit	Mata
Item	Syllibol	Min.	Max.	Omi	Note
Digital Power Supply Voltage	$V_{CC}$	$V_{SS}$ -0.3	5.0	V	-

#### 7.2.2 Backlight Unit

 $(Ta=25\pm2^{\circ}C)$ 

Italia	C11	Value		I I:4	NI-4-
Item	Symbol	Min.	Max.	Unit	Note
Forward current	If	-	(50)	mA	(1)
Reverse voltage	$V_R$	-	(25)	V	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.



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### 8. Electrical Characteristics 8.1 TFT-LCD Module

 $(Ta=25\pm2^{\circ}C)$ 

Item	Crystals of		Value		Unit	Note
nem	Symbol	Min.	Тур.	Max.	Unit	Note
Power Supply Voltage	V <sub>CC</sub>	2.5	3.3	3.6	V	-
Power Supply Current	$I_{CC}$	1	15.6	22.0	mA	(1)
Input High Threshold Voltage	$V_{\mathrm{IH}}$	$0.8V_{\rm CC}$	-	$V_{CC}$	V	-
Input Low Threshold Voltage	$V_{\rm IL}$	0	-	0.2V <sub>CC</sub>	V	-
Power Consumption	$P_{\rm L}$	-	51.48	72.6	mW	(1)
VSYNC Frequency	$F_{V}$	-	60	90	Hz	-
HSYNC Frequency	$F_{H}$	-	15.72	22.35	KHz	-
DCLK Frequency	DCLK	-	6.5	10	MHz	-

Note (1) The specified power consumption is under the conditions at  $V_{CC}$ =3.3V,  $F_{V}$ =60Hz, whereas a power dissipation check pattern below is displayed.

### Black Pattern / 0 Gray



Active Area



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# 8.2 Backlight Unit

(Ta=25±2°C)

Itama	Cryssals of		Value	Unit	Note	
Item	Symbol	Min.	Тур.	Max.	Onit	Note
LED Voltage	VL	-	(16.5)	-	V	(1)
LED Current	IL	ı	(40)	ı	mA	(1)
Power Consumption	$P_{\mathrm{BL}}$	-	(660)	-	mW	(1)
LED Life Time(25°C)	-	10000	30000	-	hr	(2)

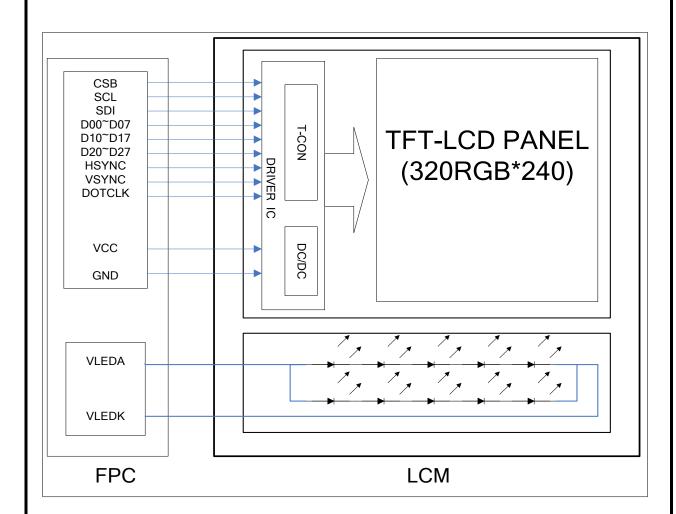
Note (1) The driving design of backlight unit is dependent on serial consideration of 5S2P LEDs.

Note (2)LED life time is defined as under 25±2°C, when the average brighten decrease to 50% of original brightness



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# 9. Block Diagram TFT-LCD Module with Backlight Unit





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# 10. Input / Output Terminals Pin Assignment 10.1 TFT-LCD Module (CVILUX CF25601D0R0-05)

Pin No.	Symbol	I/O	Description		
1	LED_K	I	LED_cathode		
2	LED_K	I	LED_cathode		
3	LED_A	I	LED_anode		
4	LED_A	I	LED_anode		
5	GND	I	Ground		
6	NC	I	No connection		
7	NC	I	No connection		
8	NC	I	No connection		
9	NC	I	No connection		
10	GND	I	Ground		
11	NC	I	No connection		
12	NC	I	No connection		
13	NC	I	No connection		
14	RESET	I	Reset		
15	CSB	I	CHIP SELECT		
16	SCL	I	Serial Clock		
17	SDI	I	Serial Data		
18	D20	I	Blue data(LSB)		
19	D21	I	Blue data		
20	D22	I	Blue data		
21	D23	I	Blue data		
22	D24	I	Blue data		
23	D25	I	Blue data		
24	D26	I	Blue data		
25	D27	I	Blue data(MSB)		
26	D10	I	Green data(LSB)		
27	D11	I	Green data		
28	D12	I	Green data		
29	D13	I	Green data		
30	D14	I	Green data		
31	D15	I	Green data		
32	D16	I	Green data		



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Pin No.	Symbol	I/O	Description
33	D17	I	Green data(MSB)
34	D00	I	Red data(LSB)
35	D01	I	Red data
36	D02	I	Red data
37	D03	I	Red data
38	D04	I	Red data
39	D05	I	Red data
40	D06	I	Red data
41	D07	I	Red data(MSB)
42	HSYNC	I	Horizontal synchronous signal
43	VSYNC	I	Vertical synchronous signal
44	DOTCLK	I	Data Colck
45	NC	I	No connection
46	NC	I	No connection
47	$V_{CC}$	I	Vdigital
48	$V_{CC}$	I	Vdigital
49	NC	I	No connection
50	NC	I	No connection
51	NC	I	No connection
52	NC	I	No connection
53	NC	I	No connection
54	NC	I	No connection
55	NC	I	No connection
56	NC	I	No connection
57	NC	I	No connection
58	NC	I	No connection
59	GND	I	Ground
60	GND	I	Ground



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# 10.2 Color Data Input Assignment

The brightness of each primary color(red, green and blue) is based on the 8 bit gray scale data input for the color. The higher the binary input, the brighter the color. The table provides the assignment of color versus data input.

		Data Signal																							
	Color					ed							Gr								Bl				
	1	D07	D06	D05	D04	D03	D02	D01	D00	D17	D16	D15	D14	D13	D12	D11	D10	D27	D26	D25	D24	D23	D22	D21	D20
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
RED	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Scale Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	ı : I	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	. : !	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



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# 11. Interface Timing

# 11.1 Input Signal Characteristics

# 11.1.1 Digital Parallel RGB Interface (960×240 resolution)

Characteristics		Symbol	Min. 24 bit	Typ. 24 bit	Max. 24 bit	Unit	
DOTCLK Frequence	У	fDOTCLK	->->-	6.5	10	MHz	
DOTCLK Period	44. (	tDOTCLK	100	154	-	ns	
Horizontal Frequen	cy (Line)	fH	-	14.9	22.35	KHz	
Vertical Frequency	(Refresh)	fV	-	60	90	Hz	
Horizontal Back Po	rch	tHBP	-	68	-	tDOTCLK	
Horizontal Front Po	rch	tHFP	-	20	-	tDOTCLK	
Horizontal Data Sta	rt Point	tHBP	-	68	-	tDOTCLK	
Horizontal Blanking	Period	tHBP + tHFP	-	88	-	tDOTCLK	
Horizontal Display Area		HDISP	-	320	-	tDOTCLK	
Horizontal Cycle		Hcycle	-	408	450	tDOTCLK	
Vertical Back Porch	1	tVBP	-	18	•	Lines	
Vertical Front Porch	ı	tVFP	-	4	-	Lines	
Vertical Data Start	Point	tVBP	-	18	-	Lines	
Vertical Blanking P	eriod	tVBP + tVFP	-	22	-	Lines	
Vertical Display NTSC				240			
Area	PAL	VDISP	-	280(PALM=0)	-	Lines	
Alea	PAL			288(PALM=1)			
Vertical Cycle	NTSC	Vcycle	-	262	350	Lines	
vertical cycle	PAL	v cycle		313	330	Lilles	

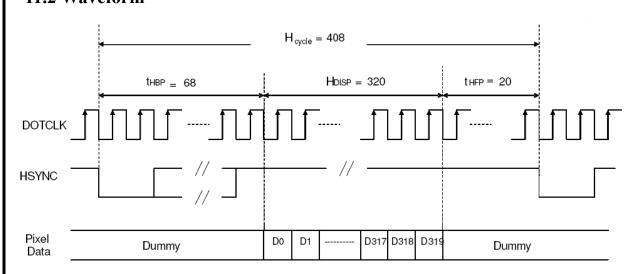
# 11.1.2 SPI Interface

Characteristics	Symbol	Min.	Тур.	Max.	Unit
Serial Clock Frequency	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Clock Rising Time	trs	-	-	30	ns
Clock Falling Time	tfl	-	-	30	ns
Chip Select Setup Time	tcss	0	-	-	ns
Chip Select Hold Time	tcsh	10	-	-	ns
Serial Clock Frequency Serial Clock Cycle Time Clock Low Width Clock High Width Clock Rising Time Clock Falling Time Chip Select Setup Time Chip Select Hold Time Data Setup Time	tcsd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns

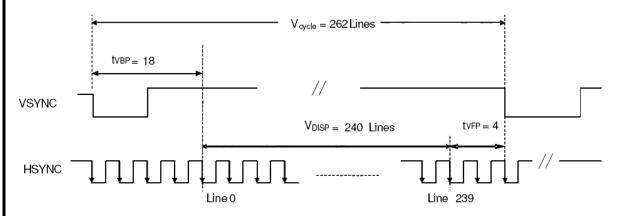


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#### Horizontal Data Transaction Timing



Vertical Data Transaction Timing

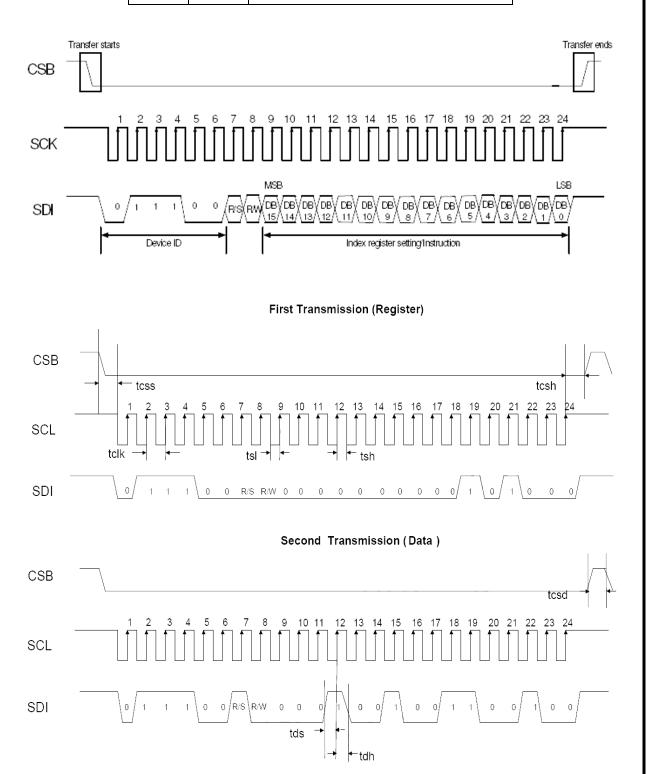
Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)



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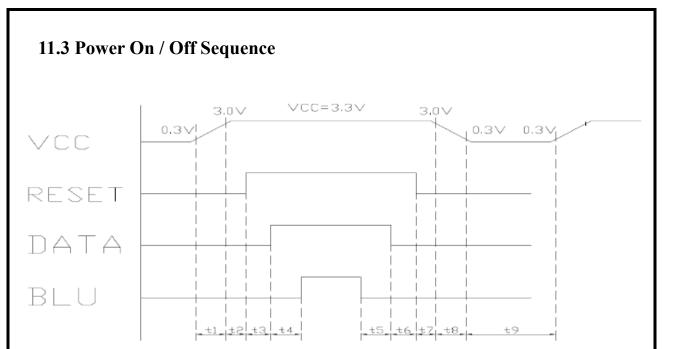
### 11.2.2 SPI

R/S	R/W	status
0	0	Write SPI address
1	0	Write SPI data





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T1≦10ms 200ms≦T5 1 sec≦T9 10us≦T2 50ms≦T6 50ms≦T3 10us≦T7 200ms≦T4 T8≦10ms



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# 12. Instruction Description **SPI Command Table**

					-			·		·			T	T		T			
Reg#	Register		R/S	IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0
SR	Status Read	1	0	L7	L6	L5	L4	L3	L2	L1	L0	0	0	0	0	0	0	0	0
R01h	Driver output control	0	1	0	RL	REV	PINV	BGR	SM	TB	CPE	0	0	0	0	0	0	0	0
R02h	LCD driver AC control	0	1	0	0	0	0	0	0	B/C	0	0	0	0	0	0	0	0	0
R03h	Power control (1)	0	1	DCT3	DCT2	DCT1	DCT0	BTF	BT2	BT1	BT0	DC3	DC2	DC1	DC0	AP2	AP1	AP0	0
R04h	Data and color filter control	0	1	0	0	0	0	0	PALM	BLT1	BLTO	OEA1	OEA0	SEL2	SEL1	SEL0	SWD2	SWD1	SW Do
R05h	Function control	0	1	GHN	XDK	GDIS	LPF	DEP	CKP	VSP	HSP	DEO	DIT	0	PWM	0	FB2	FB1	FB0
R06h	Reserved									Re	served								
R07h	Reserved									Re	served								
RoAh	Contrast/ Brightness control	0	1	0	BR6	BR5	BR4	BR3	BR2	BR1	BRo	0	0	0	CON4	CON3	CON2	CON1	CONO
RoBh	Frame cycle control	0	1	NO1	NO0	SDT1	SDT0	0	EQ2	EQ1	EQ0	0	0	0	0	0	0	0	0
RoDh	Power control (2)	0	1	0	VRC2	VRC1	VRC0	0	0	VDS1	VDS0	0	0	VRH5	VRH4	VRH3	VRH2	VRH1	VRH0
RoEh	Power control (3)	0	1	0	0	1	VDV6	VDV5	VDV4	VDV3	VDV2	VDV1	VDVo	0	0	0	0	0	0
R0Fh	Gate scan starting Position	0	1	0	0	0	0	0	0	0	0	SCN7	SCN6	SCN5	SCN4	SCN3	SCN2	SCN1	SCN0
R16h	Horizontal Porch	0	1	XLIM8	XLIM7	XLIM6	XLIM5	XLIM4	ХLIМЗ	XLIM2	XLIM1	XLIM0	0	0	0	0	0	0	0
R17h	Vertical Porch	0	1	STH1	STH0	HBP6	HBP5	HBP4	НВРз	HBP2	HBP1	HBP0	VBP6	VBP5	VBP4	VBP3	VBP2	VBP1	VBP0
R1Eh	Power control (4)	0	1	0	0	0	0	0	0	0	0	nOTP	VCM6	VCM5	VCM4	VCM3	VCM2	VCM1	VCMo
R27h	Reserved										served								
R28h	Reserved										served								
R29h	Reserved										served								
R2Bh	Reserved	-	_						PKP	PKP	served PKP			_			PKP	PKP	PKP
R30h	γ control (1)	0	1	0	0	0	0	0	12 PKP	11 PKP	10 PKP	0	0	0	0	0	02 PKP	01 PKP	00 PKP
R31h	γ control (2)	0	1	0	0	0	0	0	32 PKP	31 PKP	30 PKP	0	0	0	0	0	22 PKP	21 PKP	20 PKP
R32h	γ control (3)	0	1	0	0	0	0	0	52	51 PRP	50	0	0	0	0	0	42	41	40 PRP
R33h	γ control (4)	0	1	0	0	0	0	0	PRP 12	11	PRP 10	0	0	0	0	0	PRP 02	PRP 01	00
R34h	γ control (5)	0	1	0	0	0	0	0	PKN 12	PKN 11	PKN 10	0	0	0	0	0	PKN 02	PKN 01	PKN 00
R35h	γ control (6)	0	1	0	0	0	0	0	PKN 32	PKN 31	PKN 30	0	0	0	0	0	PKN 22	PKN 21	PKN 20
R36h	γ control (7)	0	1	0	0	0	0	0	PKN 52	PKN 51	PKN 50	0	0	0	0	0	PKN 42	PKN 41	PKN 40
R37h	γ control (8)	0	1	0	0	0	0	0	PRN 12	PRN 11	PRN 10	0	0	0	0	0	PRN 02	PRN 01	PRN 00
RзАh	γ control (9)	0	1	0	0	0	VRP 14	VRP 13	VRP 12	VRP 11	VRP 10	0	0	0	0	VRP 03	VRP 02	VRP 01	VRP 00
R3Bh	γ control (10)	0	1	0	0	0	VRN 14	VRN 13	VRN 12	VRN 11	VRN 10	0	0	0	0	VRN 03	VRN 02	VRN 01	VRN 00

Note: \* means don't care



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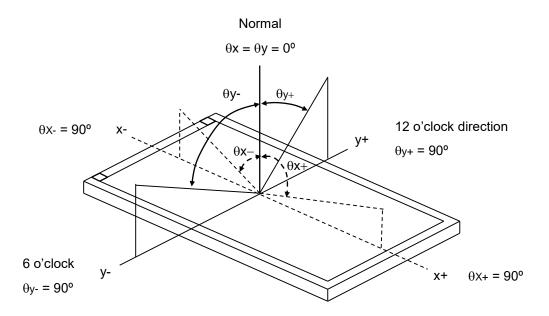
# 13. Optical Characteristics

The optical characteristics should be measured in a dark environment ( $\leq 1 \text{ lux}$ ) or equivalent state with the methods shown in Note (4).

Item	l	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Contrast Ratio		CR		300	(450)	-	-	(2)
Response Time		$T_{R+}T_{F}$		-	50	-	ms	(3)
Luminance(Center	·)	Y		700	(800)	-	cd/m <sup>2</sup>	(4)
Brightness uniforn	nity	Buni		75	(80)	-	%	(5)
	D 1	Rx	$\theta_x=0^\circ, \ \theta_Y=0^\circ$	0.562	0.612	0.662	-	
	Red	Ry	Viewing Normal	0.303	0.353	0.403	-	
	C	Gx	Angle	0.268	0.318	0.368	-	
Color	Green	Gy	2 angiv	0.525	0.575	0.625	-	
Chromaticity	Blue	Bx		0.088	0.138	0.188	-	
		Ву		0.050	0.100	0.150	-	(1) (4)
	W/l-:4-	Wx		0.265	0.315	0.365	-	(1),(4)
	White	Wy		0.311	0.361	0.411	-	
W . A 1	II:1	$\theta_{x}$ +		55	(70)	-		
	Horizontal	$\theta_{x}$ -	GD: 40	55	(70)	-	deg.	
Viewing Angle	Voution	$\theta_{Y}$ +	CR≥10	40	(55)	-		
	Vertical	θy-		50	(70)	-		

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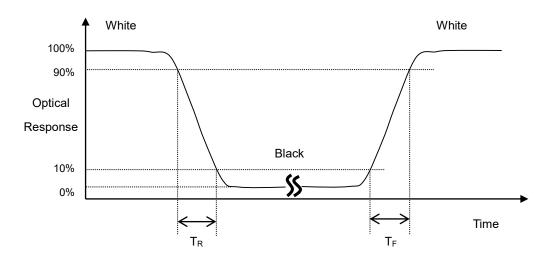
### Note (1) Definition of Viewing Angle ( $\theta x$ , $\theta y$ ):



### Note (2) Definition of Contrast Ratio (CR):

 $Contrast\ ratio\ (CR) = \frac{Luminance\ measured\ when\ LCD\ on\ the\ "White"\ state}{Luminance\ measured\ when\ LCD\ on\ the\ "Black"\ state}$ 

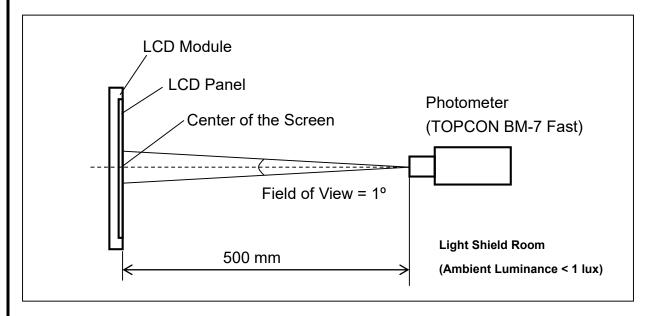
### Note (3) Definition of Response Time (T<sub>R</sub>, T<sub>F</sub>):



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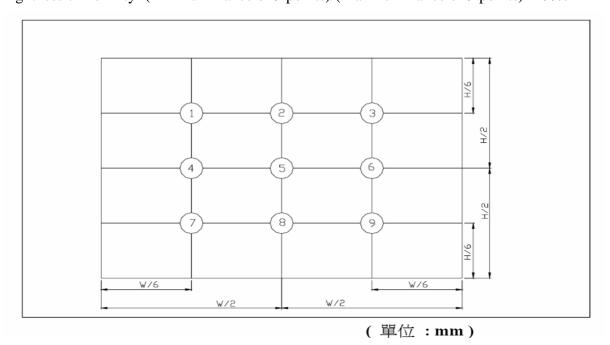
#### Note (4) Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (5) Definition of brightness uniformity

Brightness uniformity=(Min Luminance of 9 points)/(Max Luminance of 9 points)×100%





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# 14. Reliability Test

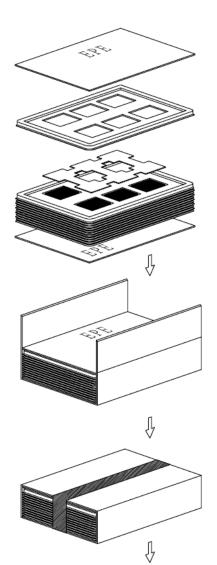
No.	Test Items	Test Condition	Remark
1	High Temperature Storage Test	T <sub>a</sub> = 80°C 240 hours	-
2	Low Temperature Storage Test	$T_a = -30$ °C 240 hours	-
3	High Temperature Operation Test	T <sub>a</sub> = 70°C 240 hours	-
4	Low Temperature Operation Test	$T_a = -20$ °C 240 hours	-
5	High Temperature and High Humidity Operation Test	T <sub>a</sub> =60°C 90%RH 240 hours	-
6	Electro Static Discharge Test (non-operating)	-Panel Surface/Top Case : 150pF, 330Ω Air: ±15kV, Contact: ±8kV	-
7	Mechanical Shock Test (non-operating)	Half sine wave, 100G, 6ms 3 times shock of each six surfaces	-
8	Vibration Test (non-operating)	Sine wave, 10 ~ 55 ~ 10Hz, 3 axis, 2 hours/axis	-
9	Thermal Shock Test (non-operating)	-20°C(30min) ~ 70°C(30min),100 cycles	-
10	Drop Test(with Carton)	Height: 80cm 1 corner, 3 edges, 6 surfaces	-

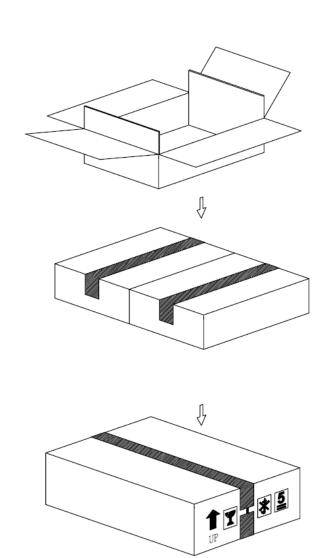


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# 15. Packaging

Packing Method





		PARTS LIST			
	ITEM	SIZE(LxWxH) unit:mm	MATERIAL	Q.T.Y	NOTE
1	TRAY	372.0x262.0x16.0	PET	28	
2	CARD BOARD(P01)	816.0x375.0x3.5	CARTON	2	
3	CARD BOARD(P02)	945.0x275.0x3.5	CARTON	2	
4	CARD BOARD(P03)	375.0x265.0x3.5	CARTON	4	
5	INTERNAL BOX(S01)	400.0x290.0x150.0	CARTON	2	
6	EXTERNAL BOX(L02)	600.0x420.0x170.0	CARTON	1	
7	EPE Form	285.0x197.0x2.0	EPE	26	
8	PRODUCT	76.9x63.9x3.3		156	

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#### 16. Precautions

### 16.1 Assembly and Handling Precautions

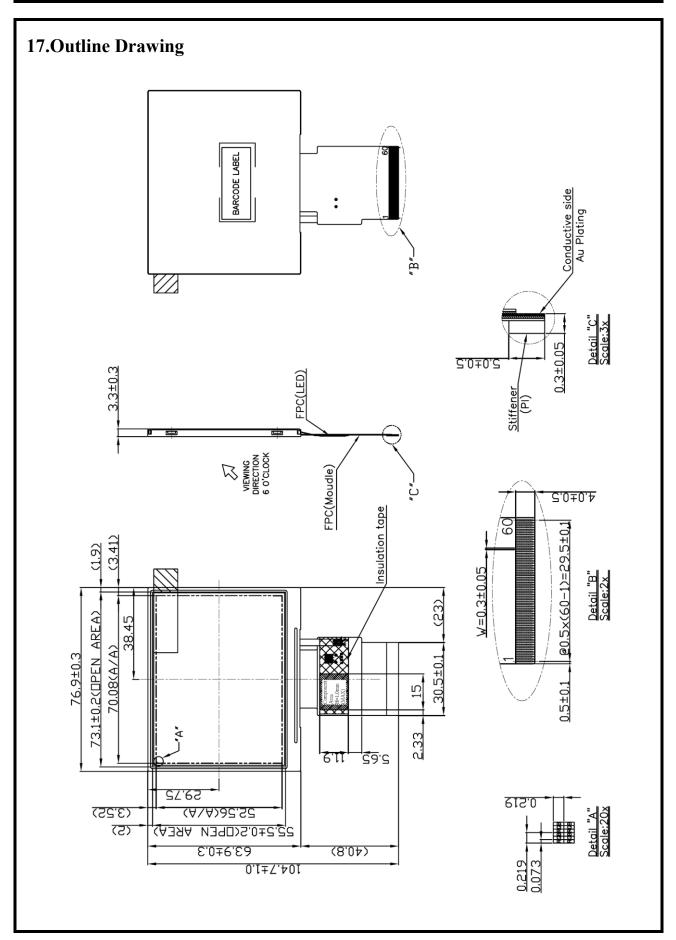
- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) It's recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) Don't apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.
- (4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
- (5) Do not plug in or pull out the I/F connector while the module is in operation.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) Moisture can easily penetrate into LCD module and may cause the damage during operation.
- (9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD module in the specified storage conditions.
- (10) When ambient temperature is lower than 10°C, the display quality might be reduced. For example, the response time will become slow.

### 16.2 Safety Precautions

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the module's end of life, it is not harmful in case of normal operation and storage.



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#### 18. Definition of Labels

The bar code nameplate is pasted on each module as illustration, and its definitions are as following explanation.

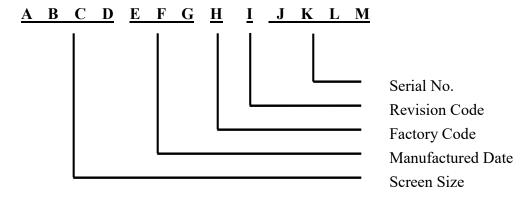


PT322435B-TLMWD-EH27



ABCDEFGHIJKLM

- (a) Module Name: PT322435B-TLMWD-EH27
- (b) Serial ID:



Serial ID includes the information as below:

(a) Screen size (Diagonal): Inch Code (ABCD)

$$3.5" \rightarrow 0350$$
 $10.4" \rightarrow 1040$ 

(b) Manufactured Date: Year, Month, Day (EFG)

#### Year (E)

Year	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009
Mark	0	1	2	3	4	5	6	7	8	9



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# Month (F)

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	В	С

### Day (G)

Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Mark	1	2	3	4	5	6	7	8	9	A	В	С	D	Е	F	G
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
Mark	Н	I	J	K	L	M	N	О	P	Q	R	S	T	U	V	

(c) Factory Code (H):

For P-TEC internal use.

(d) Revision Code (I):

Cover all the change, for example: 1: Rev.1, 2: Rev.2, 3: Rev.3...etc.

(e) Serial No. (JKLM):

Manufacturing sequence of product, for example: 0001~9999.

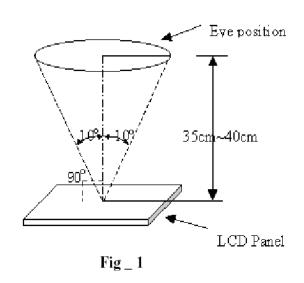
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### 19. Incoming Inspection Standards

### 19.1 The environmental condition of inspection

The environmental condition and visual inspection shall be conducted as below.

- (1) Ambient temperature  $25 \pm 5$ °C
- (2) Humidity:  $60 \pm 5\%$  RH
- (3) Viewing distance is approximately 35 ~ 40 cm
- (4) Viewing angle is normal to the LCD panel as Fig  $_1(10^\circ)$
- (5) Ambient Illumination: 300 ~ 500 Lux for external appearance inspection



# 19.2 The defects classify of AQL as following:

Class of defects	AQL	Definition
Major	0.65%	It is defect that is likely to result in failure or to reduce materially the usability of the intended function.
Minor	1.5%	It is a defect that will not result in functioning problem with deviation classified.



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# 19.3 Inspection Parameters

Item		Specification/Description			Note			
Dianlay	Function	No Display			-			
Display	Function	Malfunctio	n				-	
	Contrast ratio	Out of Spec			-			
	Line defect	No obvious Vertical and Horizontal line defect				_		
	Line defect	in bright , d	dark and co	olored.			_	
	Point Defect (red,green,blue,dark, white)	Item -		Acceptable number				
				A	В	Total		
		BRIGHT	ΓDOT	OOT $N \leq 0$ $N \leq 2$				
Operating		DARK	DOT	$N \leq 2$	N≦4	$N \leq 6$	Note:	
		TOTAL	DOT	N≦2	N≦4		1 \ 4 \ 5 \ 6	
		TWO ADJACENT DOT		NOT ALLOWED				
		THREE OI ADJACEN		NOT ALLOWED		VED		
	Scratch on the polarizer	L(mm)	W(mm)	Acceptable number				
		L≦2.5	W≦0.1	3			Note:2	
		L>2.5	W > 0.1	0				
External	Dent or bubble on the polarizer	Dimension(mm)		Acceptable number		Note:3		
Inspection (non-operating)		D≦0.3		3				
		D≦0.1		Disregard				
	Foreign material on			Note:3				
	the polarizer	D≦ D≦		Di	sregard			



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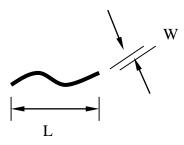
Item		Specification/Description				
ouch Panel Scratch		L(mm) W(mm) Acceptabl		Acceptable number	Note:2	
		L≦10	W < 0.05	Disregard	1	
			$0.05 \le W < 0.1$	N≦4		
			W≧0.1	0		
	Foreign		W < 0.05	Disregard	Note:2	
	Materials	L≦10	$0.05 \le W < 0.1$	N≦3		
	(Linear shape)		W≥0.1	0		
	Foreign	Dimension(mm)		Acceptable number	Note:3	
	Materials	D≦0.25		Disregard		
	(Circular shape)	$0.25 < D \le 0.5$		N≦6		
		D>0.5		0		
	Glass chipping	a c		$a \le 5.0$ mm $b \le 3.0$ mm $c \le t (t : Glass think)$	Note:7	
				$\begin{array}{l} a \! \leq \! 3.0 mm \\ b \! \leq \! 3.0 mm \\ c \! \leq \! t  (t : Glass  think) \end{array}$	Note:7	
	Newton-ring	(In case of doubtful situations) Observe on 60° from the product surface under a while Fluorescent lamp (3-wavelength lamp).		Average diameter ≤ 1/3 Touch Panel area Disregard.	Note:7	
		l				



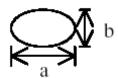
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Note1. Distance between point defect distance should be large than 5 mm.

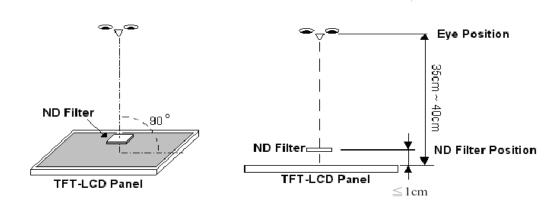
Note2.



Note3. D: Diameter D=(a+b)/2



Note4. Bright dot is defined through 6% transmission ND Filter as following.

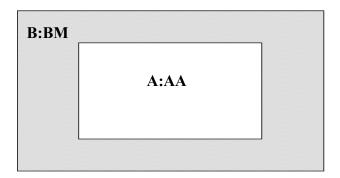


Note5. ADJACENT DOT

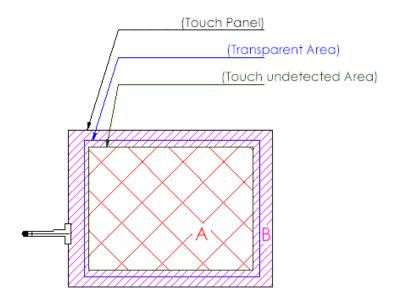




Note6.



Note7.



A area: Without any defect point effect on normal operation.

B area: None-specify

### 19.4. Handling of LCM

- (1)Don't give external shock.
- (2)Don't apply excessive force on the surface.
- (3)Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't disassemble the LCM.