PRODUCT SPECIFICATION

Part Number

PT8048102A -TLMWD-E13

CUSTOMER	
CUSTOMER PART NUMBER	
DESCRIPTION	10.2” TFT LCD
APPROVED BY	
DATE	

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2. Record of Revisions

Rev.	Comments	Page	Date
1	Preliminary Specification was first issued.	All	4/28’11

3. Module Numbering System

P T ____ ___ ___ - _ ___ _ _ - _ _ _ _ ___ _

1. 2. 3. 4. 5. 6. 7. 8. 9 10. 11. 12. 13. 14.

1. P-TEC TFT

2. LENGTH x WIDTH PIXELS
If third character is a zero, it is removed to shorten part number. Example: 240 x 320 = PT3224

3. DIAGONAL DIMENSIONS
Example: 3.5” display = 35 in part number

4. PRODUCT VERSION
Series assigned by P-tec

5. LCD MODE
T: TN
I: IPS
V: VA

6. POLARIZER
LM: Transmissive
LF: Transflective

7. BACKLIGHT COLOR
No Backlight: Left Blank
W: White
B: Blue/Green
S: Yellow/Green

8. VIEWING DIRECTION
D: 6 o’clock
U: 12 o’clock
F: Full Viewing Angle

9. A ~ Z CODE
Assigned by P-tec

11. TEMPERATURE RANGE
Normal: Left Blank
Wide: X

12. LUMINANCE
Blank: Normal (<300 nit)
M: Middle (>/= 300 nit)
H: High (> 600 nit)

13. TOUCH PANEL OPTION
No TP: Left Blank
C: Capacitive TP
R: Resistive TP

14. SPECIAL CHARACTERS
Customer special requirements

4. Application

This specification is applied to the 10.2 inch WVGA supported TFT-LCD module, and can display true 262,144 colors(6 bit/ color). The module is designed for OA, Car TV application and other electronic products which require flat panel display of digital signal interface. This module is composed of a 10.2”TFT-LCD panel, a driver circuit and LED backlight unit.

5. Features

- WVGA (800×480 pixels) resolution.
- Digital 18 bit parallel RGB.
- Dot inversion mode with stripe type.

6. General Specifications

Item	Specifications	Unit
Screen Size	10.2 (Diagonal)	inch
Display Format	800RGB(H)×480(V)	dot
Active Area	222.0(H)×132.48(V)	mm
Dot Pitch	0.0925(H)×0.276(V)	mm
Pixel Configuration	RGB Vertical Stripe	-
Display Mode	TN Type Transmissive Mode Normally White	-
Surface Treatment	Anti-Glare and Hard Coating(3H)	-
Viewing Direction	6 O’clock (The Gray Inversion will appear at this direction)	-
Outline Dimension	235.0(W)×145.8(H)×6.1(D)	mm
Weight	(332)	g
RoHS Compliance	P-TEC certifies this product to be in compliance with European Union Directive 2002/95/EC on the restriction of certain hazardous substances in electrical and electronic equipment.	-

7. Absolute Maximum Ratings

7.1 Absolute Ratings of Environment

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-30	+85	°C	(1)(2)
Operating Ambient Temperature	T_{OP}	-30	+85	°C	(1)(2)

Note1: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note2: Please refer to item of RELIABILITY.

7.2 Electrical Absolute Ratings

7.2.1 TFT-LCD Module

(Ta=25±2°C, GND=V_{SS}=0V, Note 2)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Digital Power Supply Voltage	V_{CC}	-0.3	5.0	V	-
Analog Power Supply Voltage	AV_{DD}	-0.5	12.0	V	-
Gate High Voltage	V_{GH}	13.0	19.0	V	-
Gate Low Voltage	V_{GL}	-12.0	-2.0	V	-
Gate High To Gate Low Voltage	V_{GH} - V_{GL}	-	31.0	V	-
Input signal voltage	V1~V7	0.4 AV_{DD}	AV_{DD}-0.1	V	(1)
	V8~V14	-0.3	0.6 AV_{DD}	V	

Note 1: AVDD-0.1≧V1≧V2≧V3≧V4≧V5≧V6≧V7≧V8≧V9≧V10>V11≧V12≧ V13 ≧V14≧AVSS + 0.1

7.2.2 LED Driver Absolute Maximum Ratings

(Ta=25±2°C, Note 2)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
LED Reverse Voltage	VR	-	1.2	V	Each LED (3)
LED Forward Current	IF	-	25	mA	Each LED

Note 2: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 3: VR Conditions: Zener Diode 20mA

8. Electrical Characteristics

8.1 TFT-LCD Module

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Digital Power Supply Voltage	V_{CC}	3.0	3.3	3.6	V	-
Analog Power Supply Voltage	AV_{DD}	9.0	9.2	9.4	V	-
Gate High Voltage	V_{GH}	15.3	16.0	16.7	V	-
Gate Low Voltage	V_{GL}	-7.7	-7.0	-6.3	V	-
Input signal voltage	V_{COM}	3.65	3.85	4.05	V	-
	V1~V7	0.4 AV_{DD}	-	AV_{DD}-0.1	V	-
	V8~V14	0.1	-	0.6 AV_{DD}	V	-
Digital Power Supply Current	I_{CC}	-	4	10.0	mA	(1)
Analog Power Supply Current	IAI_{DD}	-	25.0	50.0	mA	(1)
Gate High Current	I_{GH}	-	0.3	0.5	mA	(1)
Gate Low Current	I_{GL}	-	0.2	1.0	mA	(1)
Input High logic Voltage	V_{IH}	0.7 V_{CC}	-	V_{CC}	V	-
Input Low logic Voltage	V_{IL}	0	-	0.3 V_{CC}	V	-
VSYNC Frequency	F_V	-	60	-	Hz	-
DCLK Frequency	DCLK	-	40	-	MHz	-

Note (1) The specified power consumption is under the conditions at V_{CC} =3.3V, AV_{DD} =9.2V, V_{GH} =16.0V, V_{GL} =-7.0V, V_{COM} =3.85V ,F_V=60Hz, whereas a power dissipation check pattern below is displayed.

Black Pattern / 0 Gray

Active Area

8.2 Backlight Unit

(Ta=25±2°C)

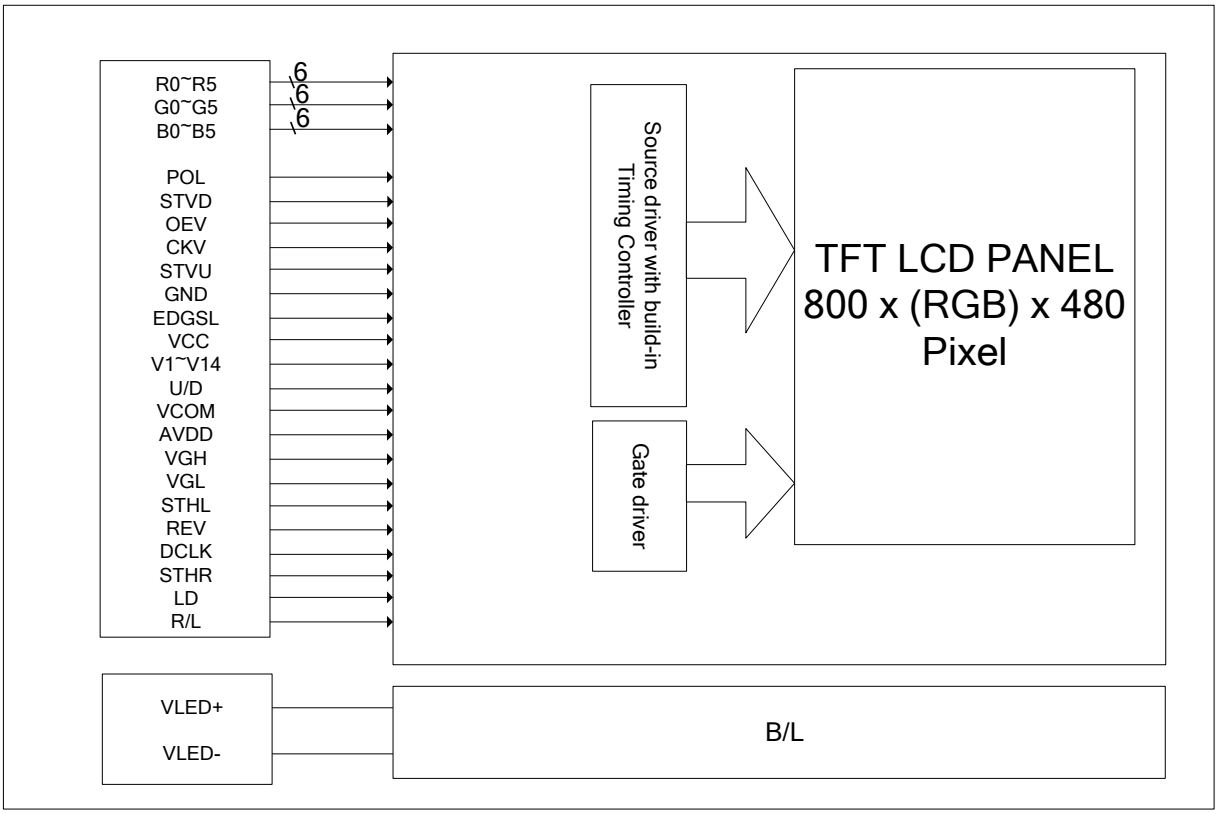
Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Current of Backlight Unit	I_B	180	200	220	mA	-
Voltage of Backlight Unit	V_B	8.4	9.3	10.5	V	(1)
Power Consumption	P_{BL}	-	(1.86)	-	W	-
LED Life Time(25℃)	-	20000	-	-	hr	(2)

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃and IL =200mA.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25℃and IL =200mA. The LED lifetime could be decreased if operating IL is lager than 200 mA.

9. Block Diagram

9.1 TFT-LCD Module with Backlight Unit



10. Input / Output Terminals Pin Assignment

10.1 TFT-LCD Module

FPC connector is used for the module electronics interface. The recommended model is "AF 730L-A2G1T" manufactured by P-TWO.

Pin No.	Symbol	I/O	Function	Remark
1	POL	I	Polarity selection	
2	STVD	I/O	Vertical start pulse input when U/D= H	Note 1
3	OEV	I	Output enable	
4	CKV	I	Vertical clock	
5	STVU	I/O	Vertical start pulse input when U/D= L	Note 1
6	GND	P	Power ground	
7	EDGSL	I	Select rising edge or rising/falling edge	
8	V_{CC}	P	Power supply for digital circuit	
9	V9	I	Gamma voltage level 9	
10	V_{GL}	P	Gate OFF voltage	
11	V2	I	Gamma voltage level 2	
12	V_{GH}	P	Gate ON voltage	
13	V6	I	Gamma voltage level 6	
14	U/D	I	Up/down selection	Note 1,2
15	V_{COM}	I	Common voltage	
16	GND	P	Power ground	
17	AV_{DD}	P	Power supply for analog circuit	
18	V14	I	Gamma voltage level 14	
19	V11	I	Gamma voltage level 11	
20	V8	I	Gamma voltage level 8	
21	V5	I	Gamma voltage level 5	
22	V3	I	Gamma voltage level 3	
23	GND	P	Power ground	
24	R5	I	Red data(MSB)	
25	R4	I	Red data	
26	R3	I	Red data	
27	R2	I	Red data	
28	R1	I	Red data	

29	R0	I	Red data(LSB)	
30	GND	P	Power ground	
31	GND	P	Power ground	
32	G5	I	Green data(MSB)	
33	G4	I	Green data	
34	G3	I	Green data	
35	G2	I	Green data	
36	G1	I	Green data	
37	G0	I	Green data(LSB)	
38	STHL	I/O	Horizontal start pulse input when R/L = L	Note 1
39	REV	P	Control signal are inverted or not	Note 3
40	GND	I	Power ground	
41	DCLK	I	Sample clock	
42	V_{CC}	P	Power supply for digital circuit	
43	STHR	I/O	Horizontal start pulse input when R/L = H	Note 1
44	LD	I	Latches the polarity of outputs and switches the new data to outputs	
45	B5	I	Blue data (MSB)	
46	B4	I	Blue data	
47	B3	I	Blue data	
48	B2	I	Blue data	
49	B1	I	Blue data	
50	B0	I	Blue data (LSB)	
51	R/L	I	Right/ left selection	Note 1,2
52	V1	I	Gamma voltage level 1	
53	V4	I	Gamma voltage level 4	
54	V7	I	Gamma voltage level 7	
55	V10	I	Gamma voltage level 10	
56	V12	I	Gamma voltage level 12	
57	V13	I	Gamma voltage level 13	
58	AV_{DD}	P	Voltage for analog circuit	
59	GND	P	Power ground	
60	V_{COM}	I	Common voltage	

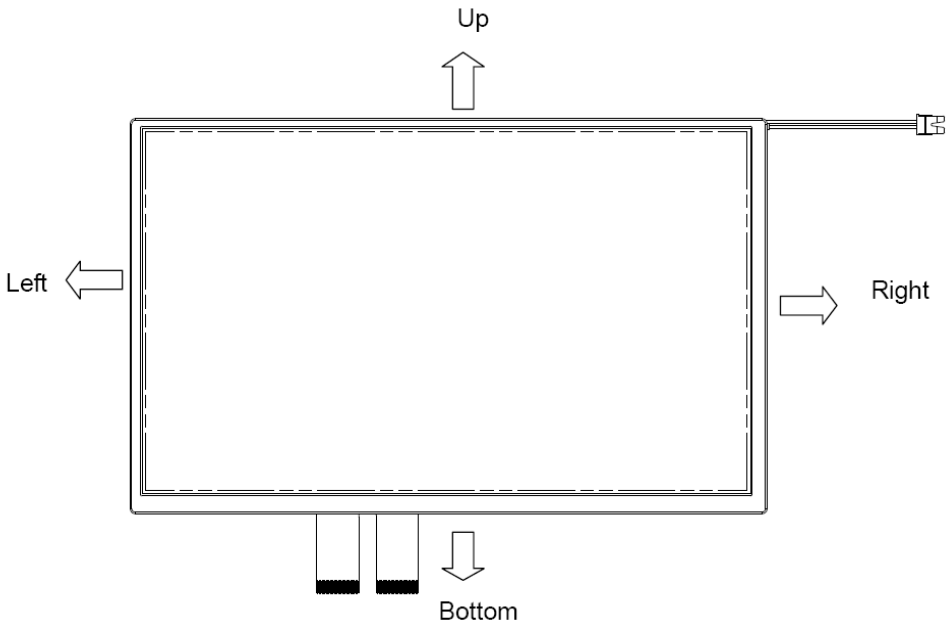
I: input, O: output, P: Power

Note 1: Selection of scanning mode

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
U/D	R/L	STVD	STVU	STHR	STHL	
GND	V_{CC}	O	I	I	O	Up to down, left to right
V_{CC}	GND	I	O	O	I	Down to up, right to left
GND	GND	O	I	O	I	Up to down, right to left
V_{CC}	V_{CC}	I	O	I	O	Down to up, left to right

Note 2: Definition of scanning direction.

Refer to the figure as below:



Note 3: When REV="L" , normally

REV="H", these data will be inverted.

10.2 Backlight Unit Section

LED Light Bar connector is used for the the integral backlight system. The recommended model is "BHSR-02VS-1" manufactured by JST.

Pin No.	Symbol	I/O	Function	Remark
1	V_{LED+}	P	Power for LED backlight anode	Pink
2	V_{LED-}	P	Power for LED backlight cathode	White

10.2 Color Data Input Assignment

The brightness of each primary color(red, green and blue) is based on the 6 bit gray scale data input for the color. The higher the binary input, the brighter the color. The table provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of RED	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
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	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

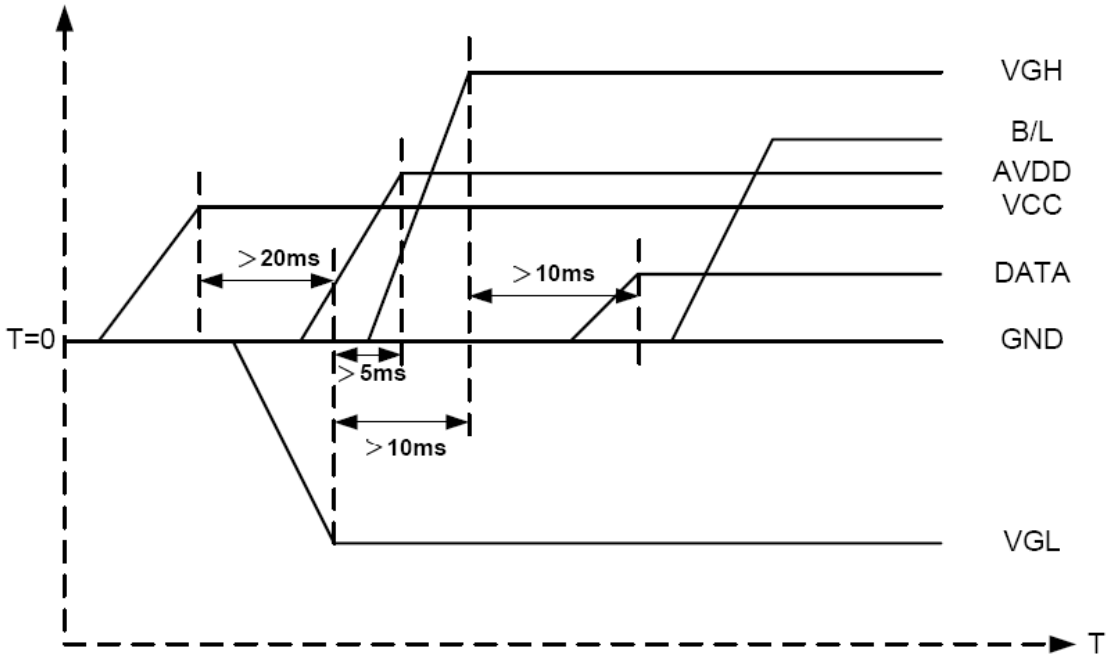
11. Interface Timing

11.1 Input Signal Characteristics

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
DCLK frequency	F_{dclk}	-	40	45	MHz	
DCLK cycle	T_{cph}	22	25	-	ns	
DCLK pulse width	T_{cw}	8	-	-	ns	
Data set-up time	T_{su}	4	-	-	ns	
Data hold time	T_{hd}	2	-	-	ns	
Time that the last data to LD	T_{ld}	1	-	-	Tcph	
Pulse width of LD	T_{wld}	2	-	-	Tcph	
Time that LD to STHL/R	T_{lds}	5	-	-	Tcph	
POL set-up time	T_{psu}	6	-	-	ns	
POL hold time	T_{phd}	6	-	-	ns	
CKV frequency	F_{vclk}	-	-	200	KHz	
CKV rise time	T_{rck}	-	-	100	ns	
CKV falling time	T_{fck}	-	-	100	ns	
CKV pulse width	P_{WCLK}	500	-	-	ns	
Horizontal display timing range	T_{dh}	-	800	-	Tcph	
Horizontal timing range	T_h	-	1056	-	Tcph	
STVU/D setup time	T_{suv}	200	-	-	ns	
STVU/D hold time	T_{hdv}	300	-	-	ns	
STVU/D delay time	T_{dt}	-	-	500	ns	
Driver output delay time	T_{do}	-	-	900	ns	
Output rise time	T_{tlh}	-	500	1000	ns	
Output falling time	T_{thl}	-	400	800	ns	
OEV pulse width	T_{wcl}	1	-	-	us	
OEV to Driver output delay time	T_{oe}	-	-	900	ns	
Horizontal lines per field	T_v	512	525	610	Line	
Vertical display timing range	T_{vd}	-	480	-	Line	

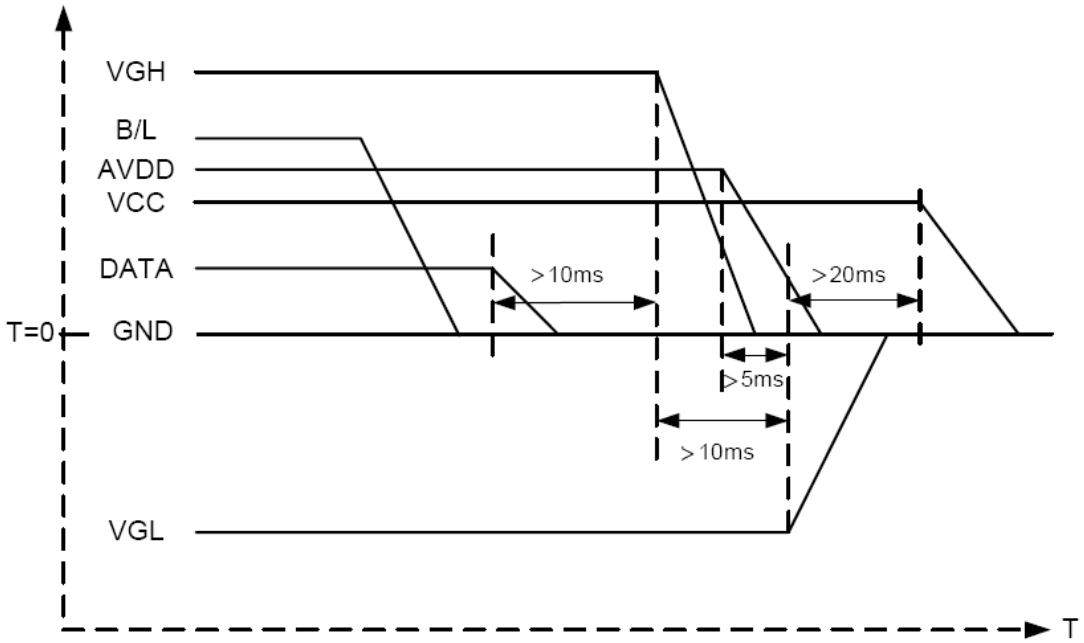
11.2 Power Sequence

Power on:



VCC→VGL→AVDD→VGH→Data→B/L

Power off:



B/L→Data→VGH→AVDD→VGL→VCC

Note: Data includes POL, STVD, OEV, CKV, STVU, EDGSL, STHL, REV, DCLK, STHR, LD.

11.3 Waveform

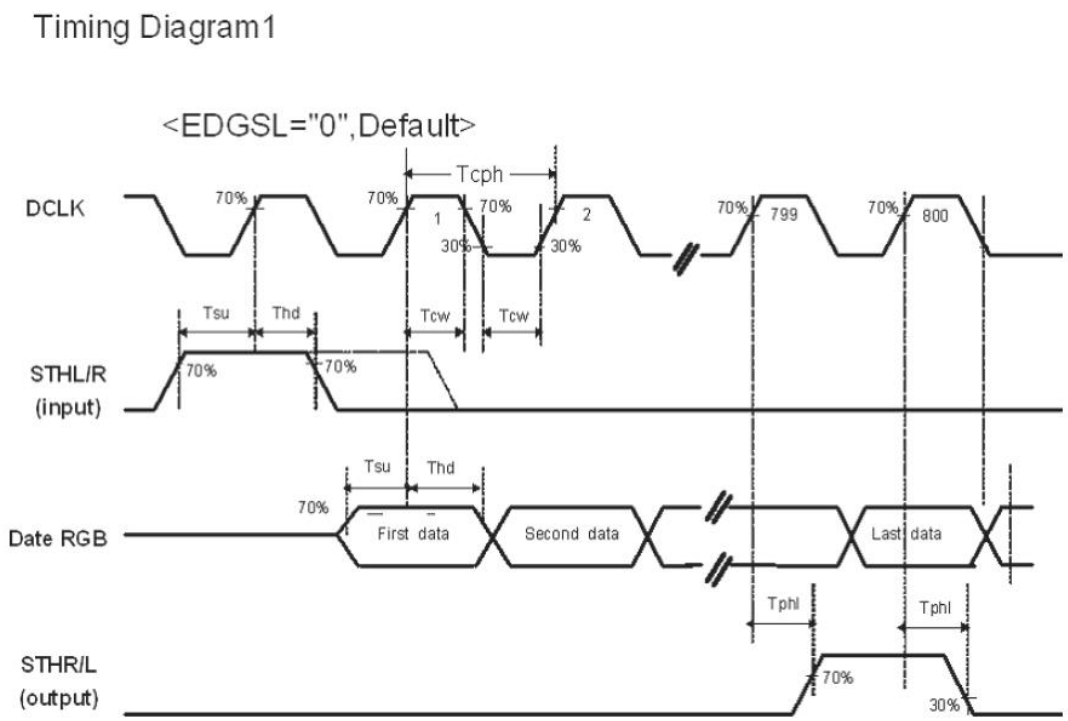


Fig.11.3-1 operation model 1

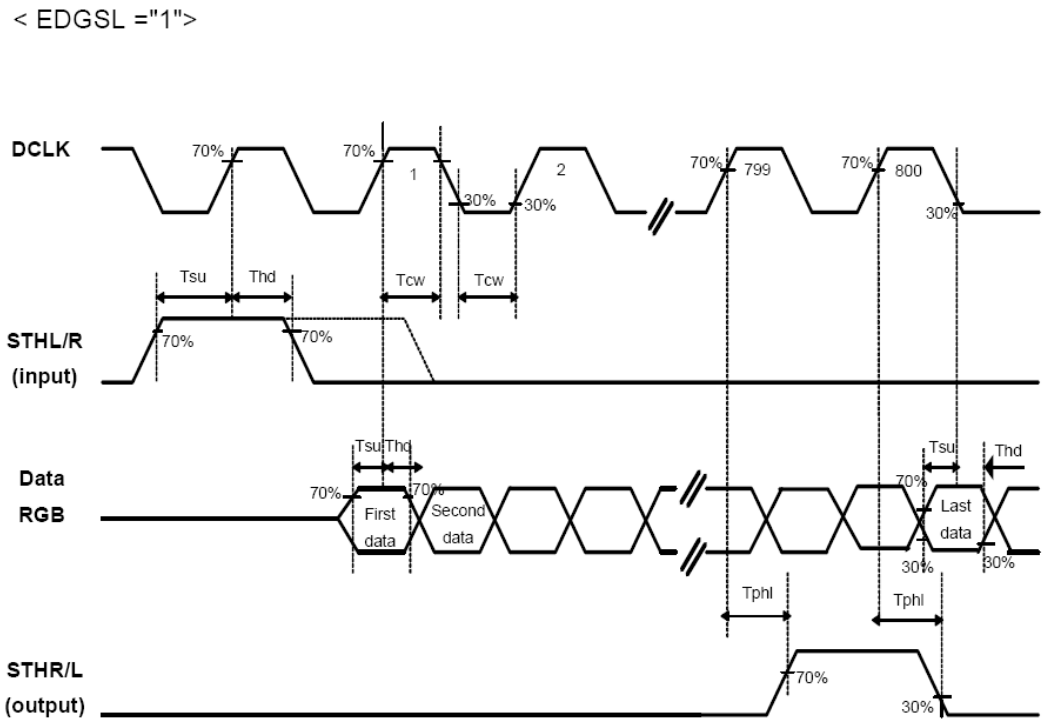


Fig.11.3-2 operation model 2

Timing Diagram 2

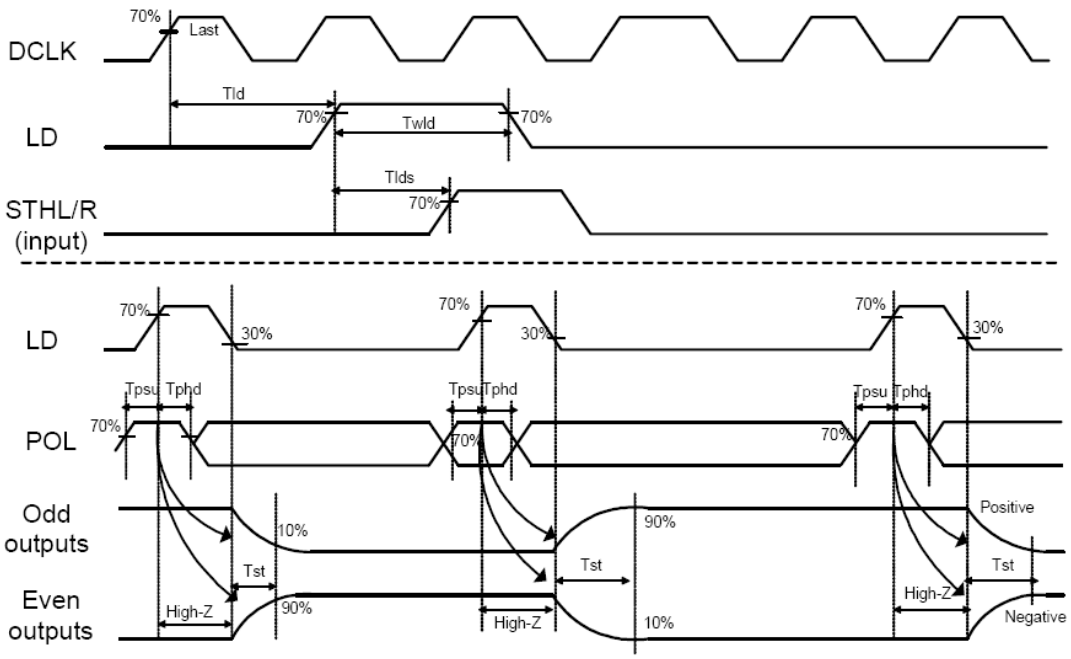


Fig.11.3-3 Horizontal timing 1

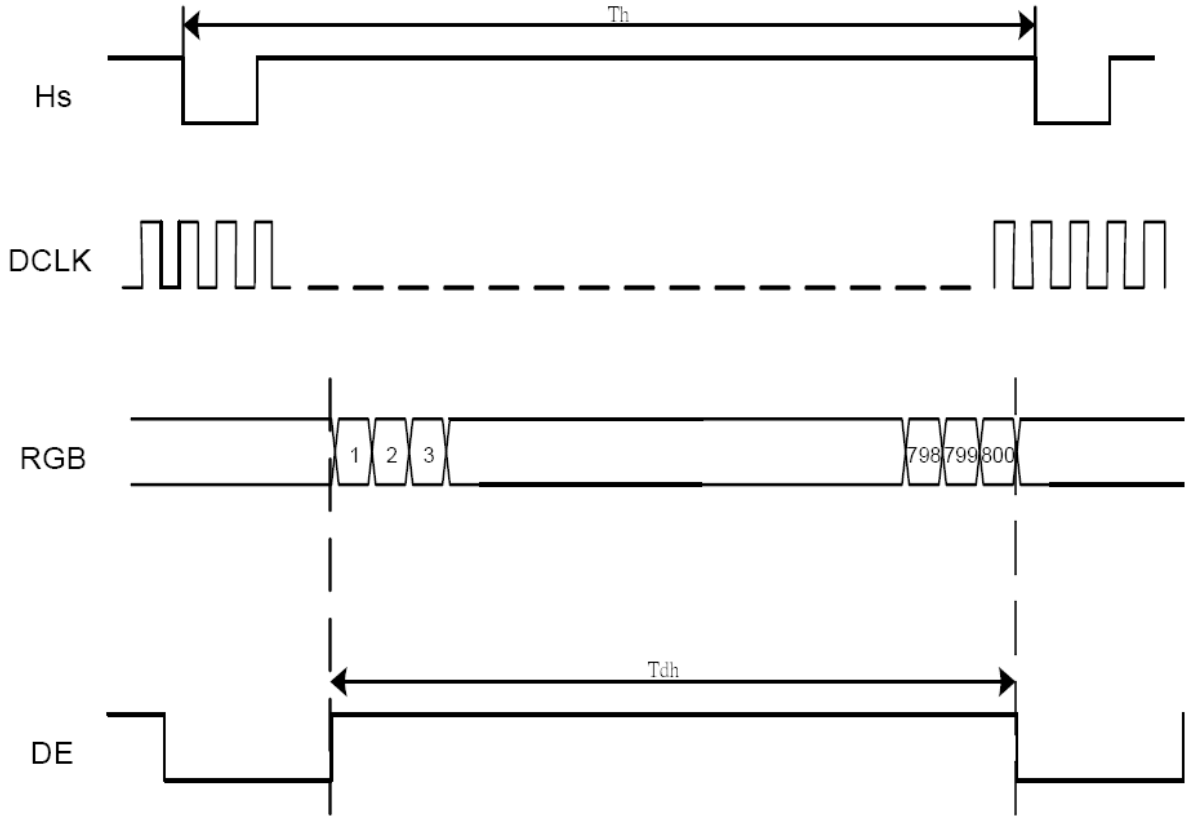


Fig.11.3-4 Horizontal timing 2

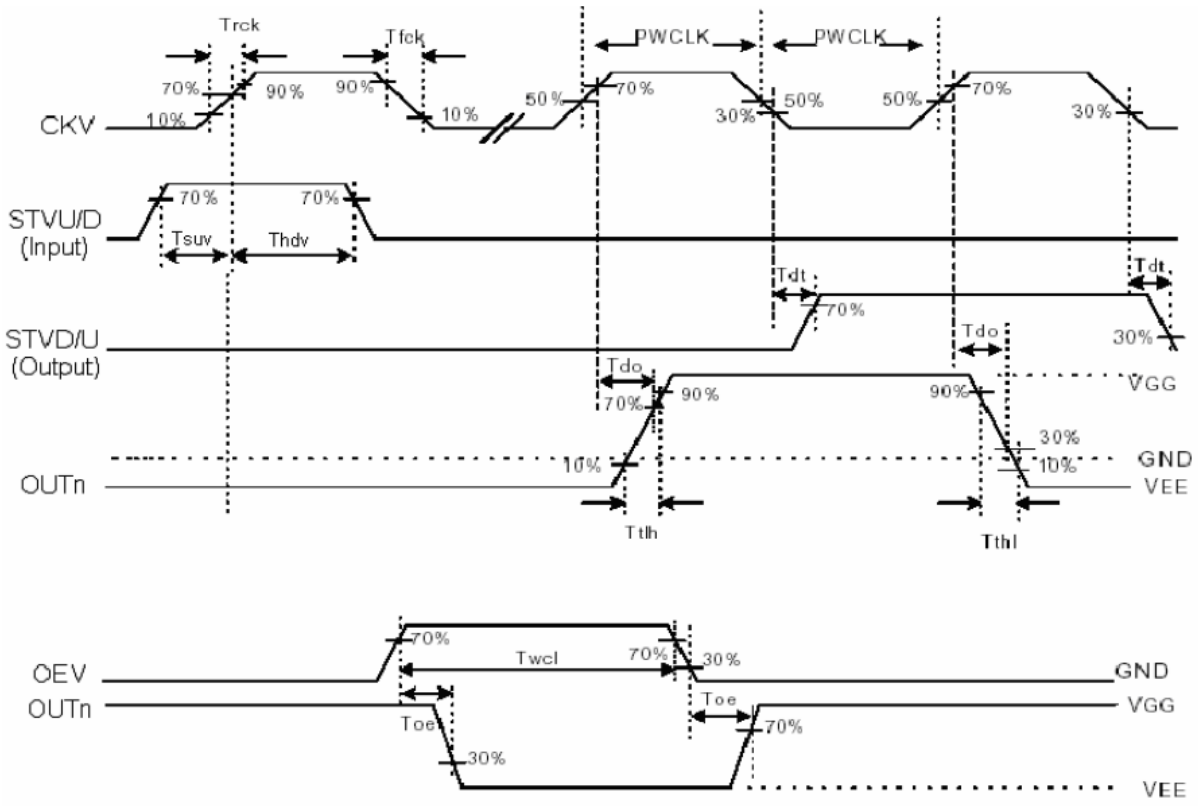


Fig.11.3-5 Vertical shift clock timing

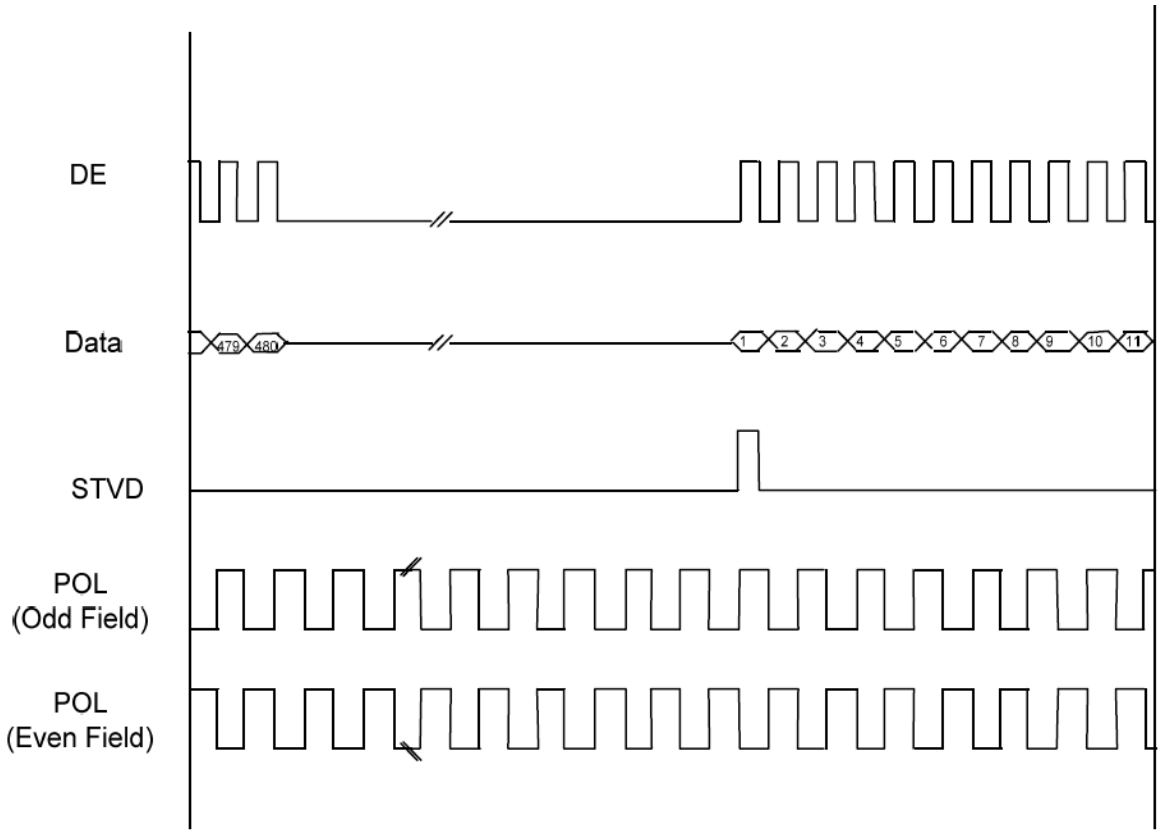


Fig.11.3-6 Vertical timing (from up to down)

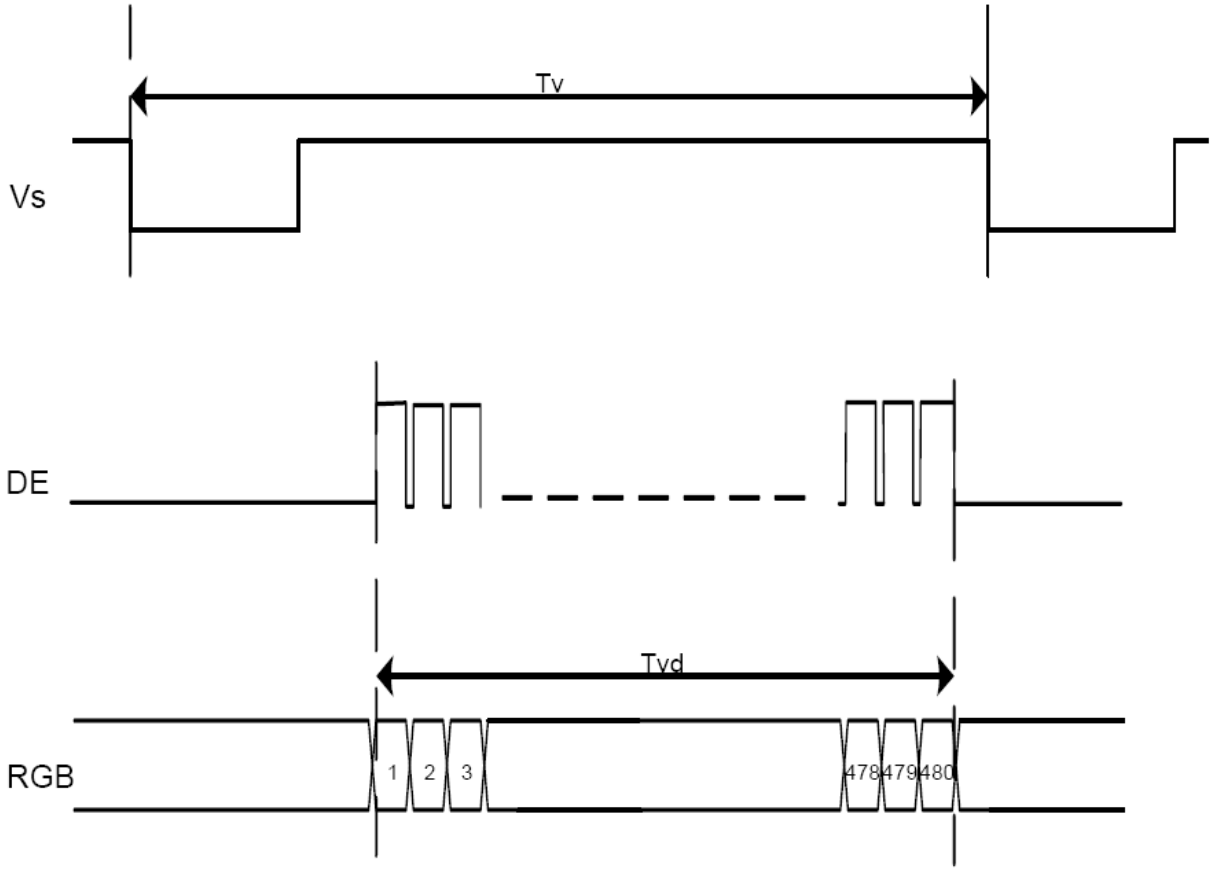


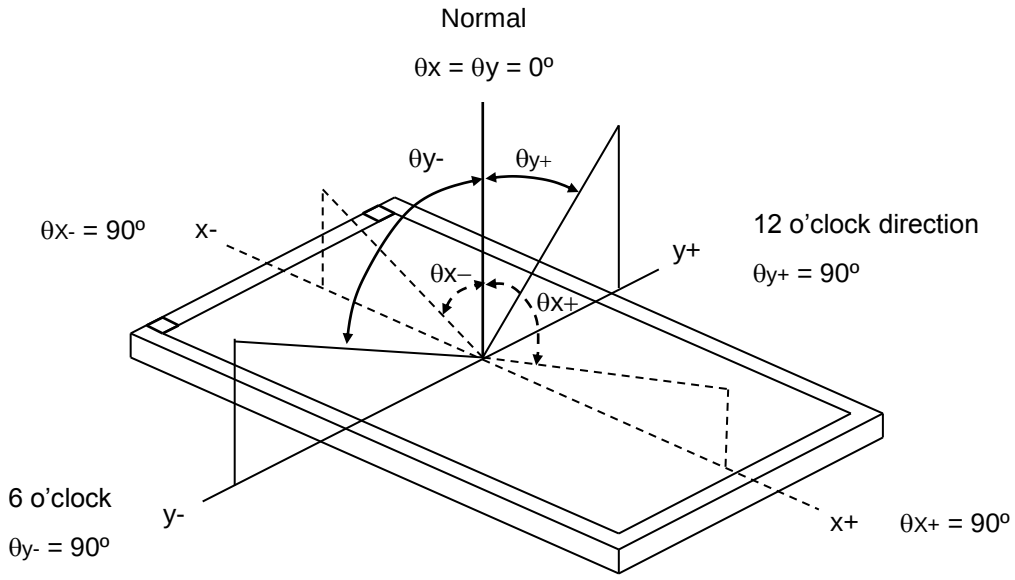
Fig.11.3-7 Vertical timing

12. Optical Characteristics

The optical characteristics should be measured in a dark environment (≦ 1 lux) or equivalent state with the methods shown in Note (4).

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	θ_X=0°, θ_Y =0° Viewing Normal Angle	250	(300)	-	-	(2)
Response Time		T_R		-	15	30	ms	(3)
		T_F		-	20	40	ms	
Luminance(Center)		Y		280	(350)	-	cd/m^2	(4)
Brightness uniformity		BUNI		70	(75)	-	%	(5)
Color Chromaticity	White	Wx		0.26	0.31	0.36	-	(1),(4)
		Wy		0.28	0.33	0.38	-	
Viewing Angle	Horizontal	θ_X+	CR≥10	55	(65)	-	deg.	
		θ_X-		55	(65)	-		
	Vertical	θ_Y+		35	(45)	-		
		θ_Y-		55	(65)	-		

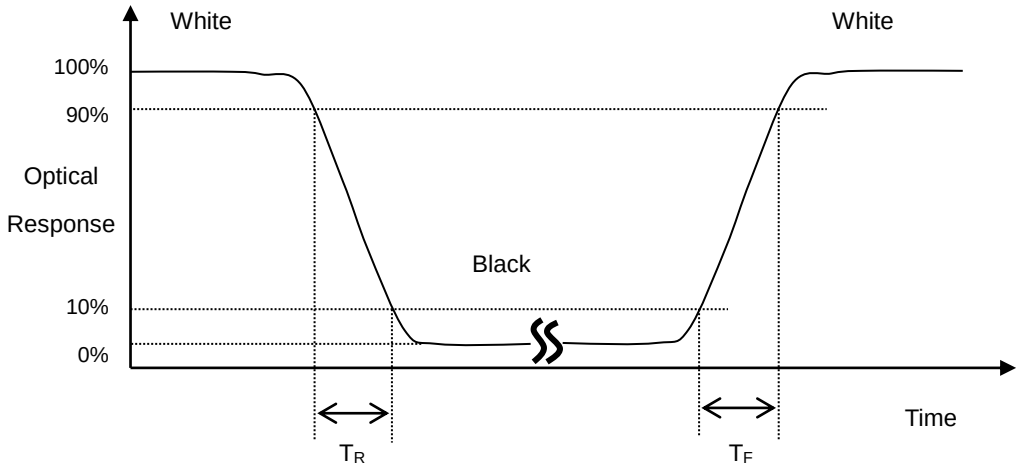
Note (1) Definition of Viewing Angle (θx, θy):



Note (2) Definition of Contrast Ratio (CR):

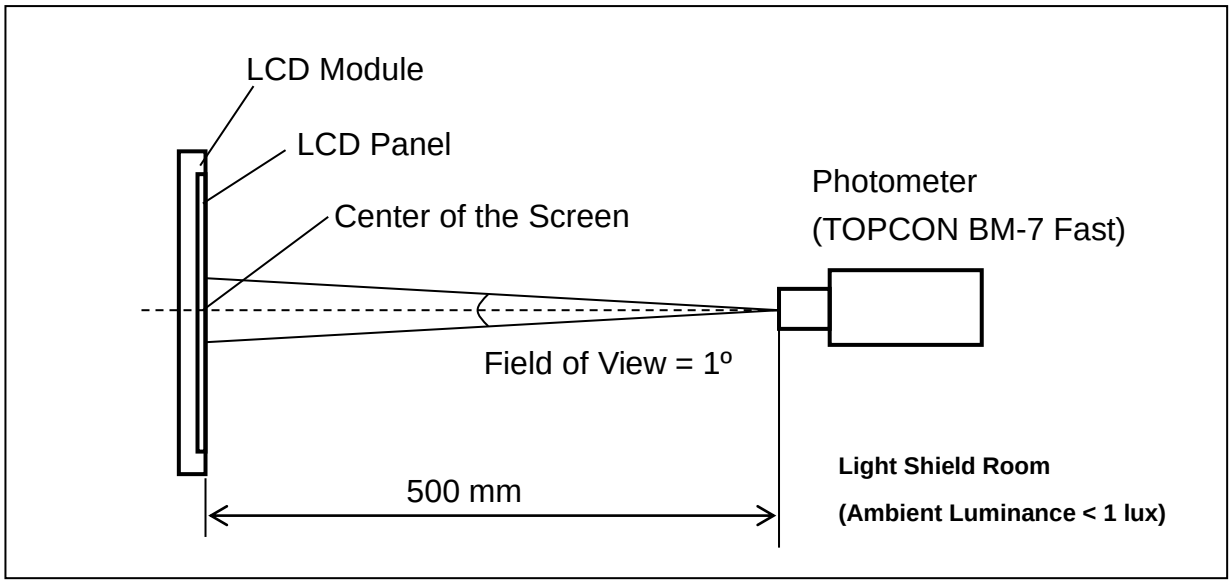
$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note (3) Definition of Response Time (T_R, T_F):



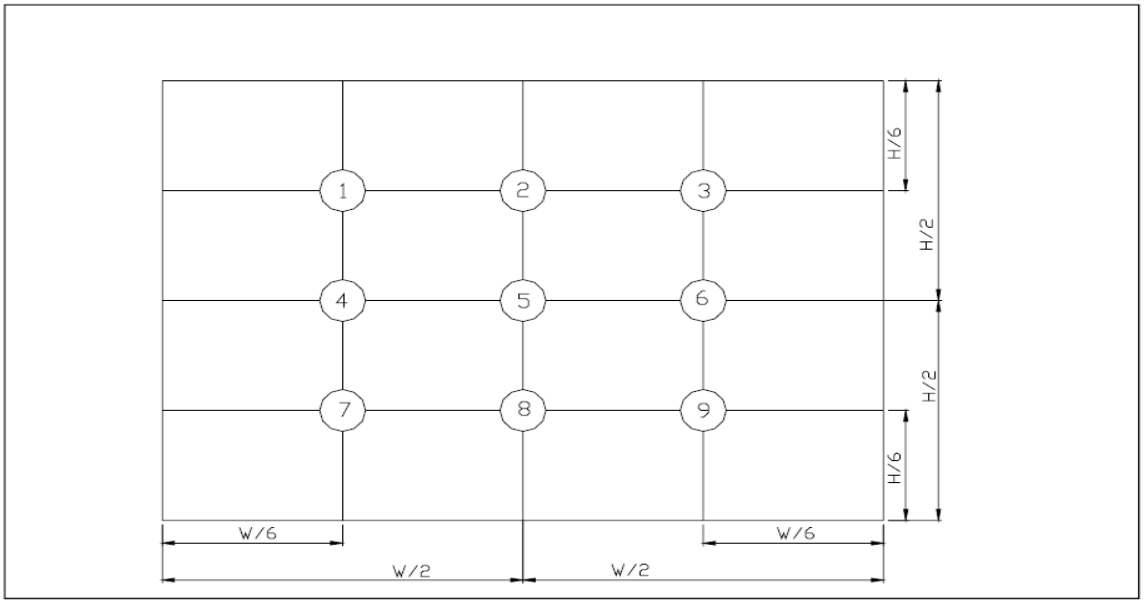
Note (4) Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (5) Definition of brightness uniformity

Brightness uniformity=(Min Luminance of 9 points)/(Max Luminance of 9 points)×100%



(單位 ：mm)

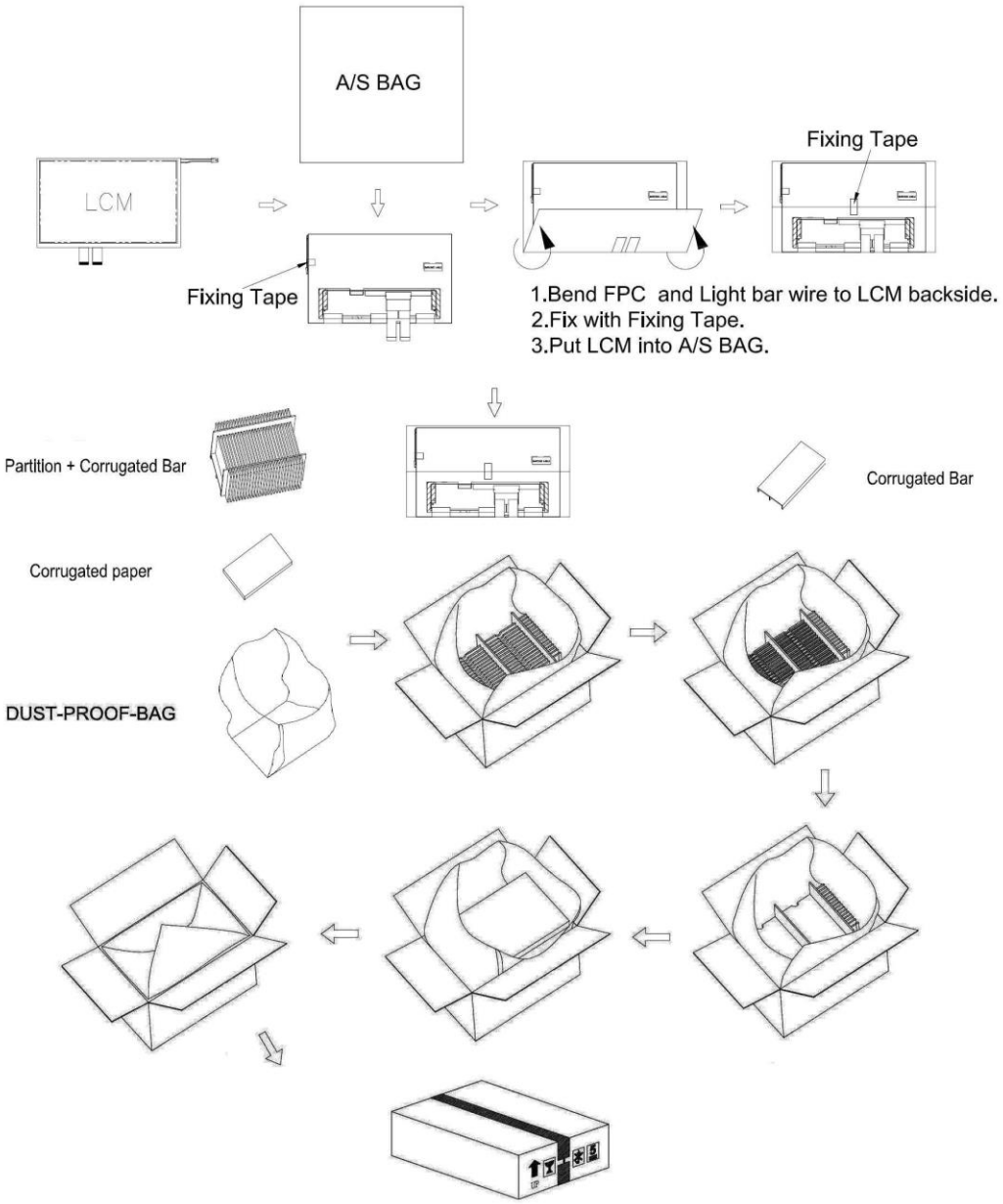
13. Reliability Test

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 85℃ 240 hrs	Note 1,Note 4
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1,Note 4
High Temperature Operation	Ts = 85℃ 240hrs	Note 2,Note 4
Low Temperature Operation	Ta =-30℃ 240hrs	Note 1,Note 4
Operate at High Temperature and Humidity	+60℃, 90%RH max. 240 hrs	Note 4
Thermal Shock	-30℃/30 min ~ +85℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.
Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

14. Packaging



	PARTS LIST				
	ITEM	SIZE(LxWxH) unit:mm	MATERIAL	Q.T.Y	NOTE
1	PARTITION	512.0x349.0x226.0	CORRUGATED PAPER	1	
2	CORRUGATED BAR	512.0x370.0x7.0	CORRUGATED PAPER	2	
3	DUST-PROOF BAG	900.0x700.0x0.05	PE	1	
4	A/S BAG	256.0x203.0x0.07	PE	25	
5	CARTON	530.0X355.0X255.0	CORRUGATED PAPER	1	
6	PRODUCT	235.0x145.8x6.1		25	

15. Precautions

15.1 Assembly and Handling Precautions

(1) Do not apply rough force such as bending or twisting to the module during assembly.

(2) It's recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.

(3) Don't apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.

(4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.

(5) Do not plug in or pull out the I/F connector while the module is in operation.

(6) Do not disassemble the module.

(7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.

(8) Moisture can easily penetrate into LCD module and may cause the damage during operation.

(9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD module in the specified storage conditions.

(10) When ambient temperature is lower than 10ºC, the display quality might be reduced. For example, the response time will become slow.

15.2 Safety Precautions

(1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

(2) After the module's end of life, it is not harmful in case of normal operation and storage.

15.3 Terms of Warrant

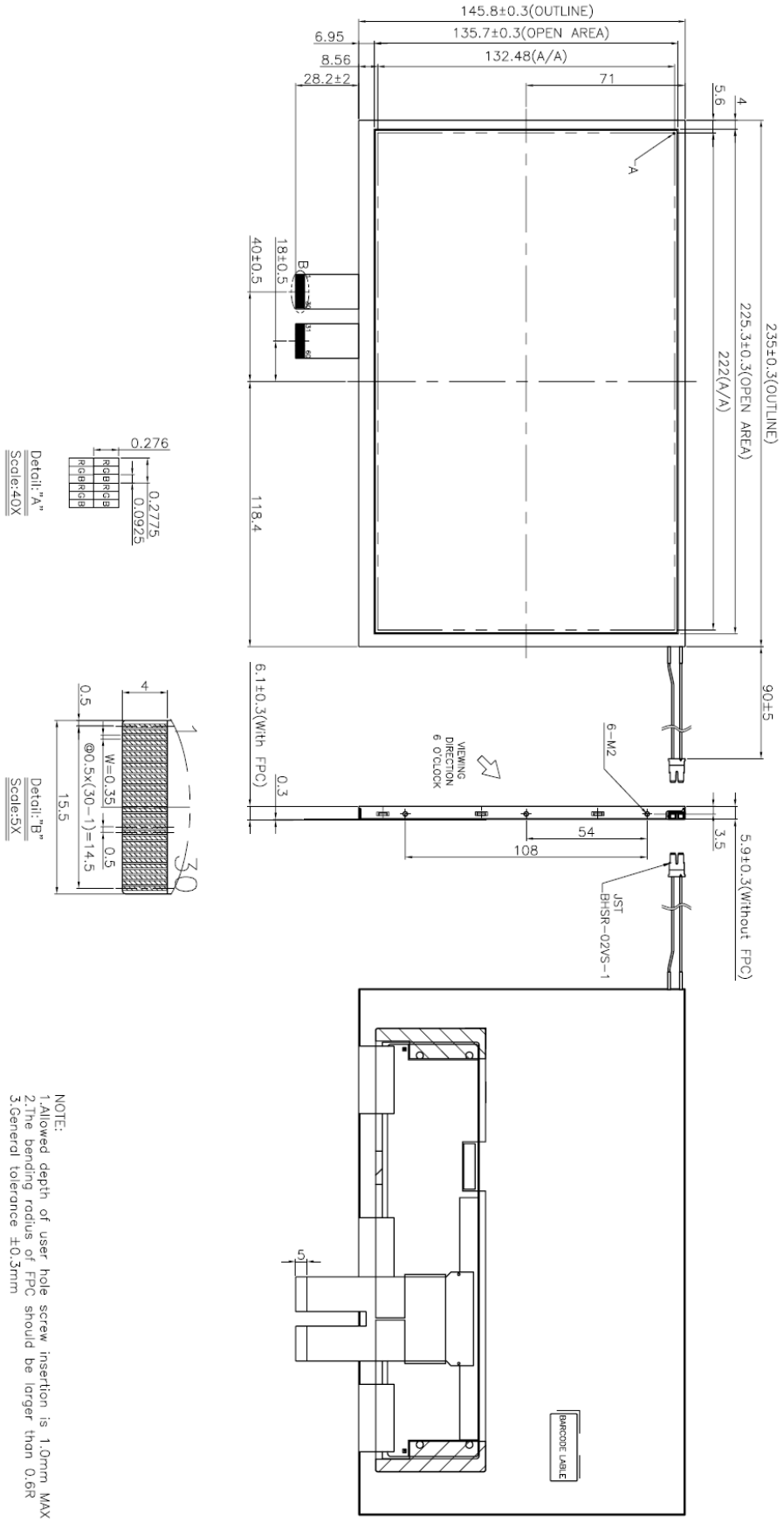
(1) Acceptance inspection period

The period is within one month after the arrival of contracted commodity at the buyer's factory site.

(2) Applicable warrant period

The period is within twelve months since the date of shipping out under normal using and storage conditions.

16.Outline Drawing



17.Definition of Labels

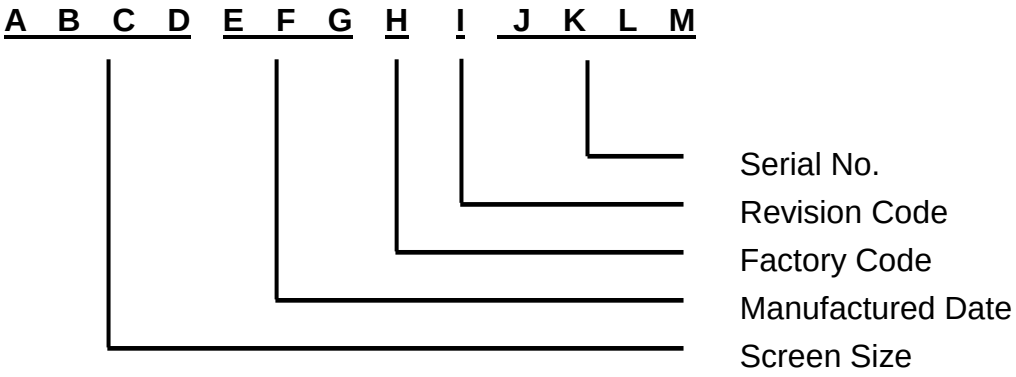
The bar code nameplate is pasted on each module as illustration, and its definitions are as following explanation.

PT8048102A-TLMWD-E13

ABCDEFGHIJKLM

(a) Module Name: PT8048102A-TLMWD-E13

(b) Serial ID:



Serial ID includes the information as below:

(a) Screen size (Diagonal): Inch Code (ABCD)

3.5” → 0350

10.4” → 1040

(b) Manufactured Date: Year, Month, Day (EFG)

Year (E)

Year	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009
Mark	0	1	2	3	4	5	6	7	8	9
Year	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019
Mark	A	B	C	D	E	F	G	H	I	J

Month (F)

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

Day (G)

Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Mark	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
Mark	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	

(c) Factory Code (H):

For P-TEC internal use.

(d) Revision Code (I):

Cover all the change, for example: 1: Rev.1, 2: Rev.2, 3: Rev.3…etc.

(e) Serial No. (JKLM):

Manufacturing sequence of product, for example: 0001~9999.

18. Incoming Inspection Standards

18.1 Inspection Parameters

1. Incoming Inspection

Both parties agree that the inspection specifications of TFT-LCD Modules (hereinafter known as "Modules") stipulated hereunder is the only and final standard applicable in the process of inspection. P-TEC shall be under no liability or obligation (including incidental loss, products liability or other consequential loss) whatsoever for any defect in quality or performance or shortage in quantity of the Modules that have passed such inspection.

2. Liability

2.1 Inspection Deadline

The Customer should inspect the Modules either at the Delivery Point or within twenty (20) calendar days after arrival at the Delivery Destination.

2.2 Notification of Rejection

The Customer may reject one or more defective or non-conforming Modules if the Modules fail to meet the AQL (Acceptable Quality Level) and pass the inspection. In that case, the customer should notify P-TEC of the rejection by either documents or mail within in three (3) business days from the date of reception of the Modules. Otherwise, the Modules shall be deemed to have met the AQL and passed the inspection.

3. Inspection Specifications

Both parties agree that the inspection shall contain and follow the inspection specifications stipulated in the attachment, including:

3.1 Scope

3.2 Sampling Plan

3.3 Panel Inspection Condition

3.4 Display Quality

3.5 Mechanics Specifications

3.6 Notification for Storage Handling

4. Limited Warranty

P-TEC represents and warrants that all Modules shall (i) conform to the specifications set hereunder, and (ii) be free from any defects in material and workmanship for twelve (12) months after the Customer’s acceptance or deemed acceptance. P-TEC will replace, rework or refund the Customer for the defective or non-conforming Modules at P-TEC’s option, provided that the Customer (i) promptly informs P-TEC of the defects or non-conformities within the warranty period, (ii) complies with the specifications and conditions hereunder, and (iii) complies with P-TEC’s procedure for Modules replacement, reworking and/or return. The warranty period for the Modules replaced

or reworked shall be the remaining term for such Modules.

5. THE WARRANTIES AND REMEDIES SET FORTH ABOVE ARE EXCLUSIVE AND IN LIEU OF ALL OTHER WARRANTIES, TERMS OR CONDITIONS, EXPRESS OR IMPLIED, EITHER IN FACT OR BY OPERATION OF LAW, STATUTORY OR OTHERWISE, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, ALL OF WHICH ARE EXPRESSLY DISCLAIMED. P-TEC'S WARRANTIES HEREIN APPLY ONLY TO THE CUSTOMER AND ARE NOT TO BE EXTENDED TO ANY THIRD PARTY.

6. Governing Law

This Agreement shall be governed and construed in accordance with the laws of the Republic of China. Both parties agree to submit any dispute, which cannot be amicably resolved, to Hsinchu District Court for the first instance.

Inspection Specifications

1. Scope

Specifications contain

1.1 Display Quality Evaluation

1.2 Mechanics Specification

2. Sampling Plan

Unless there is other agreement, the sampling plan for incoming inspection shall follow MIL-STD-105E.

2.1 Lot size: Quantity per shipment as one lot (different model as different lot).

2.2 Sampling type: Normal inspection, single sampling.

2.3 Sampling level: Level II.

2.4 AQL: Acceptable Quality Level

Major defect: AQL=0.65

Minor defect: AQL=1.0

3. Panel Inspection Condition

3.1 Environment:

Room Temperature: 25±5°C.

Humidity: 65±5% RH.

Illumination: 300 ~ 700 Lux.

3.2 Inspection Distance:

35±5 cm

3.3 Inspection Angle:

The vision of inspector should be perpendicular to the surface of the Module.

3.4 Inspection time：

Perceptibility Test Time: 20 seconds max.

4. Display Quality

4.1 Function Related:

The function defects of line defect, abnormal display, and no display are considered Major defects.

4.2 Bright/Dark Dots:

Defect Type	Specification	Major	Minor
Bright Dots	**N≤ 3**		•
Dark Dots	**N≤ 4**		•
Total Bright and Dark Dots	**N≤ 6**		•

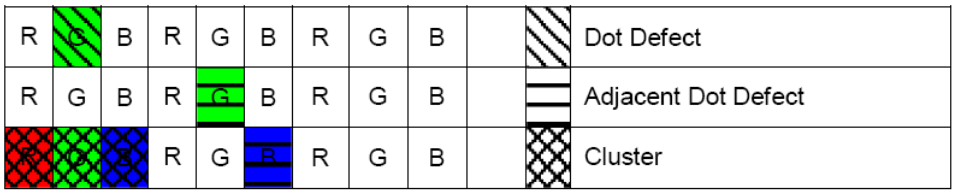
Note: 1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.

Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.

The bright dot defect must be visible through 2% ND filter

Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.

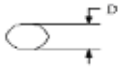
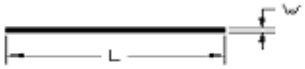
4.3 Pixel Definition:

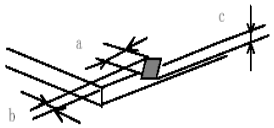
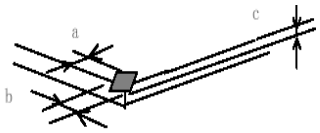
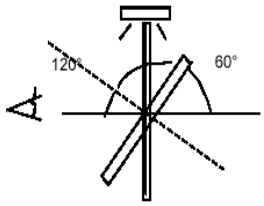


Note 1:If pixel or partial sub-pixel defects exceed 50% of the affected pixel or sub-pixel area, it shall be considered as1 defect.

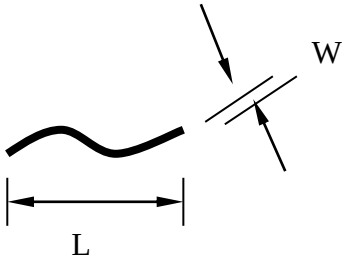
Note 2: There should be no distinct non-uniformity visible through 2% ND Filter within 2 sec inspection times.

4.4Visual Inspection specifications:

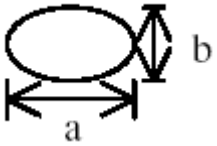
Defect Type		Specification Size	Count(N)	Major	Minor
Dot Shape (Particle、Scratch and Bubbles in display area) 		D ≤0.3 mm	Ignored		•
		0.3mm < D ≤ 0.5mm	N ≤ 4		
		D > 0.5mm	N=0		
Line Shape (Particles、Scratch、Lint and Bubbles in display area) 		W ≤ 0.07 mm	Ignored		•
		0.07mm< W ≤ 0.1mm and L ≤ 5mm	N ≤4		
		W > 0.10mm or L > 5mm	N=0		
Bubble in cell (active area)		It should be found by eyes			•
Bezel	Scratch	No harm			•
	Dirt				•
	Wrap	No harm			•
	Sunken	No harm			•
Label	No label	No			•
	Inverted label				•
	Broken				•
	Dirt	Word can be read.			•
	Not clear	No			•
	Word out of shape				•
	Mistake	No			•
	Position	Be attached on right position			•
Screw	Not enough	No			•
	Limp	No			•

Item		Specification/Description			Note
Touch Panel	Scratch	L(mm)	W(mm)	Acceptable number	Note:1
		L≦10	W＜0.05	Disregard	
			0.05≦W＜0.1	N≦4	
			W≧0.1	0	
	Foreign Materials (Linear shape)	L≦10	W＜0.05	Disregard	Note:1
			0.05≦W＜0.1	N≦3	
			W≧0.1	0	
	Foreign Materials (Circular shape)	Dimension(mm)		Acceptable number	Note:2
		D≦0.25		Disregard	
		0.25＜D≦0.5		N≦6	
		D＞0.5		0	
	Glass chipping			a≦5.0mm b≦3.0mm c≦t (t：Glass think)	Note:3
				a≦3.0mm b≦3.0mm c≦t (t：Glass think)	Note:3
	Newton-ring	(In case of doubtful situations) Observe on 60° from the product surface under a while Fluorescent lamp (3-wavelength lamp). 		Average diameter ≦1/3 Touch Panel area Disregard.	Note:3
	Membrane Drum			H≦0.4mm	

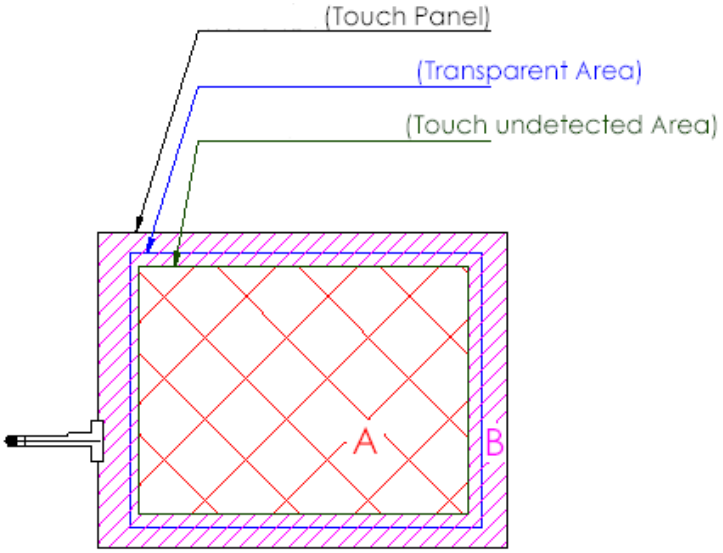
Note1.



Note2. D：Diameter D=(a+b)/2



Note3.



A area : Without any defect point effect on normal operation.

B area : None-specify

Connector	Connection status	No bend on pins and damage		•
FPC/FFC	Broken	No		•

Note: Extraneous substance and scratch not affecting the display of image, for instance, extraneous substance under polarizer film but outside the display area, or scratch on metal bezel and backlight module or polarizer film outside the display area, shall not be considered as defective or non-conforming.

5. Mechanics specifications

As for the outside dimensions and weight of the Modules, please refer to product specifications for more details.

6. Notification for Storage Handling

6.1 Storage:

6.1.1 Environment condition must be within the product specifications, otherwise the Module might be damaged.

6.1.2 Pile of stacking shall follow the instruction of P-TEC.

6.2 Handling:

6.2.1 Twisting or Bending of the Module is prohibited.

6.2.2 All chemicals are unfit for use unless otherwise instructed by P-TEC.

6.2.3 Plugging in & unplugging:

The power must be turned off before plugging in or unplugging the Module.

6.2.4 ESD protection:

The Module must not be touched without proper grounding.

6.2.5 High Voltage:

The rear side of Module must not be touched without protection.

6.2.6 Power sequence:

Shall follow the instruction of P-TEC.

18.2 Handling of LCM

(1)Don't give external shock.

(2)Don't apply excessive force on the surface.

(3)Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.

(4)Don't operate it above the absolute maximum rating.

(5)Don't disassemble the LCM.